

Compal Confidential

Hasswell M/B Schematics Document

Intel ULV Processor with LPDDR3/DDRIIIL

Date : 2013/06/26

Version 1.0

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2011/06/29	Deciphered Date	2011/06/29	Title	Cover Page
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				INS35596872	1.0
				Date: Saturday, July 06, 2013	Sheet 1 of 60

Compal Confidential
Model Name : Haswell

File Name :
LA-9314PR10(NonCS)

(nonCS)

WLAN(MiniPCIe slot) P23

Port 3

Port 3 (Reserved) USB2.0
480Mb/s

(NonCS:USB)

Touch screen P20

Port 5 USB2.0
480Mb/s

(NonCS)

Int.KBD

Touch Pad

FAN

Lid switch

Accelerometer
HP3DC2 P46

ENE KB9012 P42

PS2

SMBUS
100KHz

SPI ROM
8M P7

SPI
50MHz

VRAM

P37~P40

Nvidia
N14-GV2
17W
P32~P36

PCI-Ex4
Lane 7-Lane10
PCIe 2.0:5Gb/s
PCIe 3.0:8Gb/s

LVDS panel
P20

RTD2136S P18

eDPx2
2.7Gb/s

HDMI Conn
DDPB port P19

HDMI
222.75MHz

PCI-E Card reader
RTS5237 P25

Lane 4 PCI-E
PCIe 1.0:2.5Gb/s
PCIe 2.0:5Gb/s

10/100/1G LAN
RTL8161G P24

Lane 5
PCIe 1.0:2.5Gb/s
PCIe 2.0:5Gb/s

Lane 11 PCIe 1.0:2.5Gb/s
PCI-E

Haswell
Ultra Light & Thin

1168P BGA

(USW ULT)

Dual Channel

DDR3L 1333MHz 1.35V

DDR3-SO-DIMM X 2

P15,16

(nonCS)

SATA 3.0

Port 2

2.5" SATA HDD P22

GEN1 1.5Gb/s
GEN2 3Gb/s
GEN3 6Gb/s

Port 3

SSD(MiniPCIe slot) P22

USB3.0

5Gb/s

USB2.0

480Mb/s

Port 0

USB2.0 port P26

Port 1

Port 1

USB2.0 port P26

Port 2

Port 2

USB charger P27

USB2.0 port

Port 4

Camera P20

Port 6

WWAN(NGFF_B) P21

SIM conn

HDA 24MHz

HDA Audio codec
92HD91 P28

Audio Sub

Sub conn

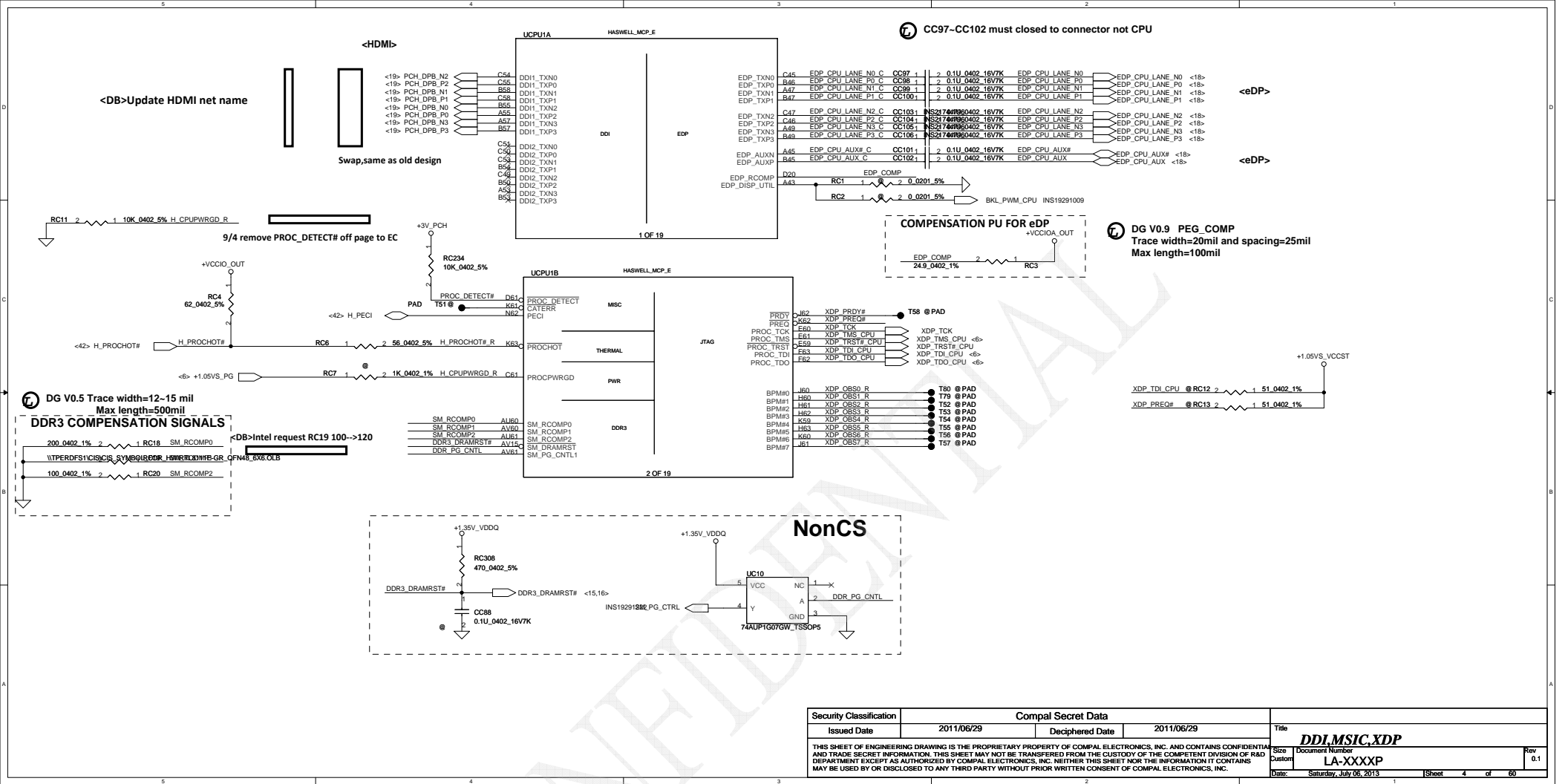
(nonCS)

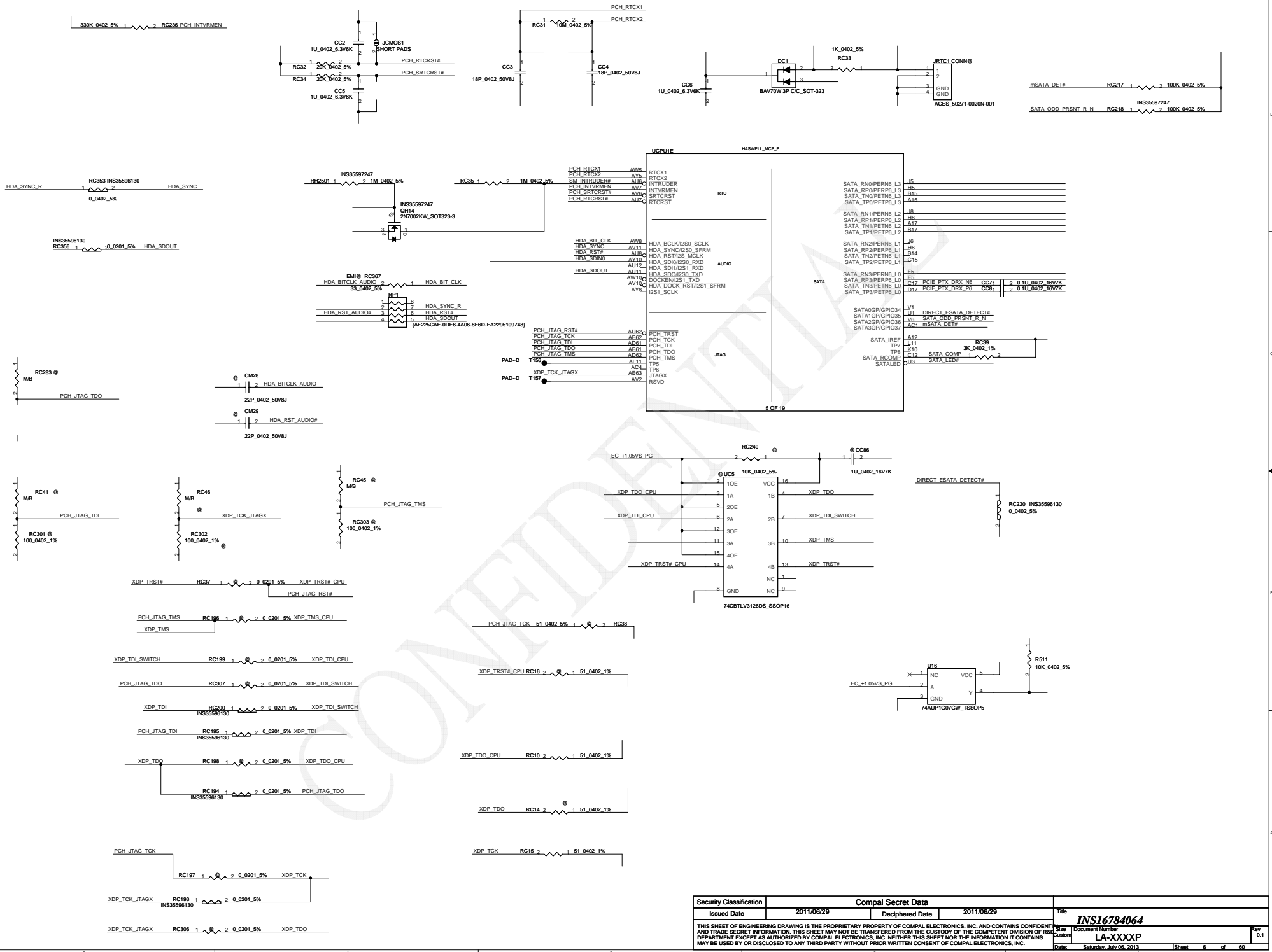
SMBUS
1MHz

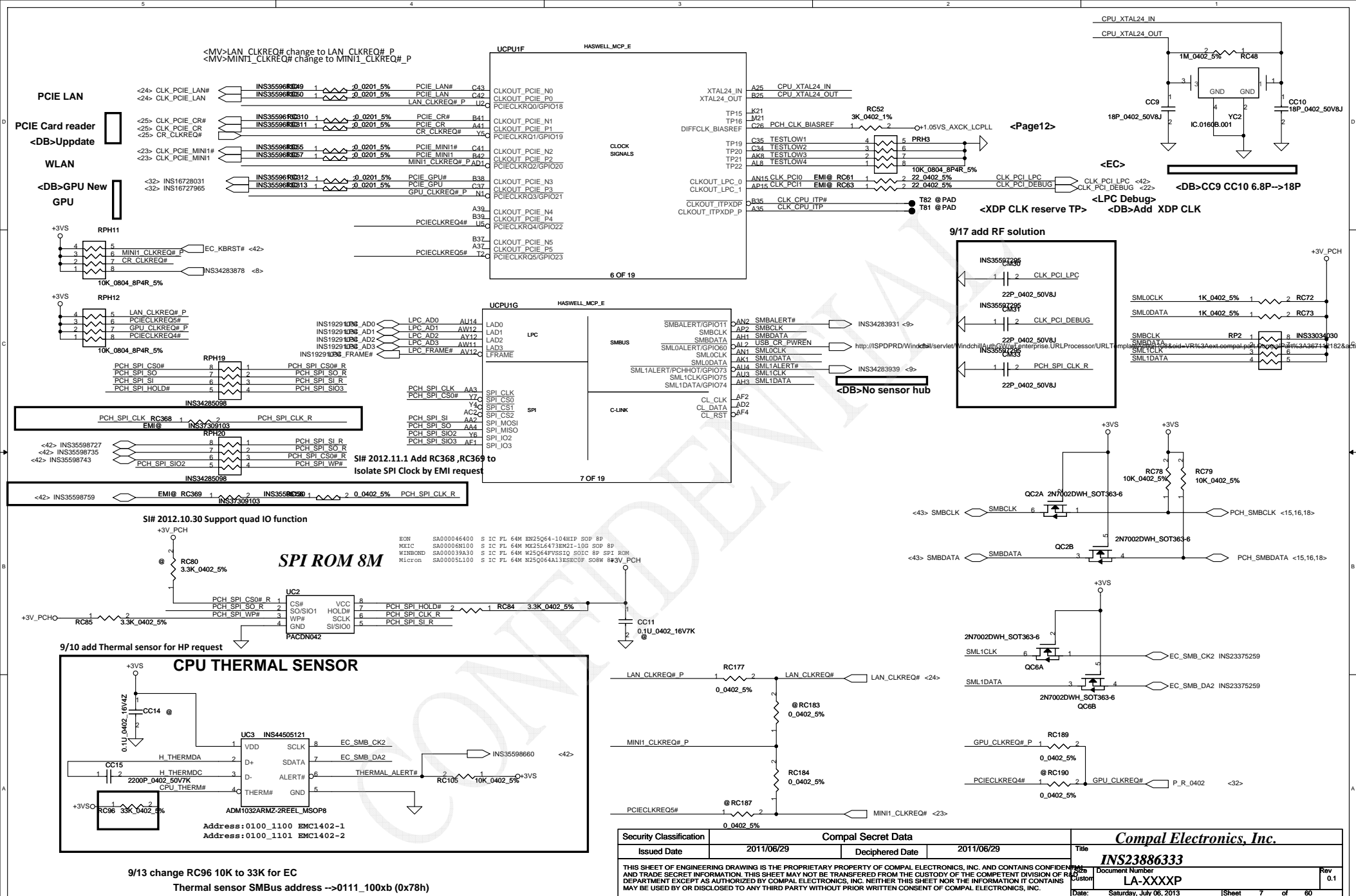
HP Amp P30

HP&MIC jack

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Non Deep S3 RC91-->SMT
Deep S3 RC93-->SMT

DSWODVREN - On Die DSW VR Enable

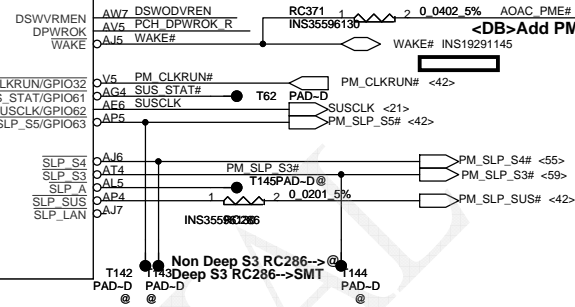
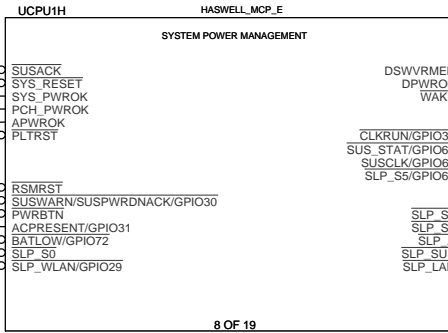
* H : Enable DSWODVREN RC254 2 330K 0.402 5%
L : Disable DSWODVREN RC255 2 330K 0.402 5%

<9> INS34283947 SUSWARN# R RC91 1 0.0201 5%

<42> SUSACK# INS35596130 1 0.0201 5% SUSACK# R AK2C
<7> INS34283878 SYS RESET# AC3C
<42> SYS_PWROK INS35596130 1 0.0402 5% SYS_PWROK AC2C
<42> PCH_PWROK INS35596130 1 0.0402 5% PM_PWROK R AV7
PLT_RST# PCH AG7C

<42> PCH_RSMRST# INS35596130 1 0.0402 5% PCH_RSMRST# AW6C
<42> PCH_SUSWARN# INS35596130 1 0.0402 5% SUSWARN# R AL7C
<42> PBTN_OUT# CH751H-40PT_SOD323-2 2 DC2
<52.53> ACIN# CH751H-40PT_SOD323-2 2 ACIN R AJ8
PM_BATLOW# AN4C
PM_SLP_S0# AF3C
PCH_SLP_WLAN# AM5C

Deep S3



PV# 2013.02.07 power rail to +3V_DSW_P

WAKE# RC98 1 1K 0.402 5%

<DB>RC106 10K-->100K
<SI>RC106 100K-->10K

PCH_RSMRST# RC106 2 10K 0.402 5%
SUSCLK RC107 2 1K 0.402 5%

Deep S3:DSW power choose on page12

+3V_DSW_P

<DB>RC112 10K-->100K

RC112 2 100K 0.402 5% SYS_PWROK

PCH_RSMRST# CH751H-40PT_SOD323-2 2 DC3 PCH_PWROK

PCH_DPWROK_R RC316 1 0.0201 5% PCH_DPWROK <42>

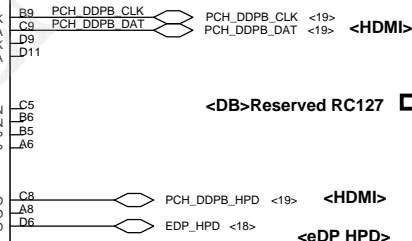
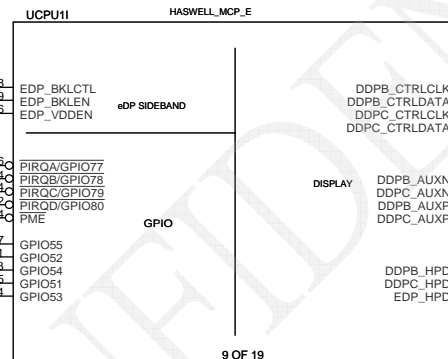
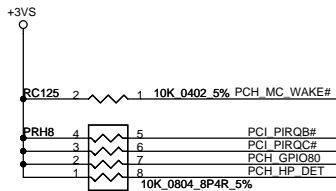
PANEL_BKEN_CPU PD 100K on Page20

<4> BKL_PWM_CPU INS35596130 1 0.0402 5% BKL_PWM_CPU R B8
<42> ENBKL ENVDD_CPU INS35596130 1 0.0402 5% ENBKL CPU A9
ENVDD_CPU INS35596130 1 0.0402 5% ENVDD_CPU R C6

9/12 change connect to ACCEL_INT# on GPIO080

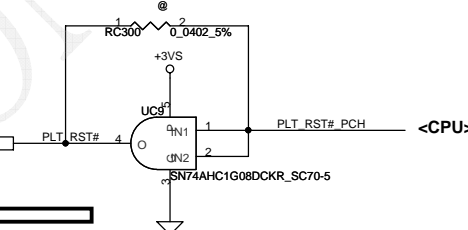
RC120 1 100K 0.402 5% ENVDD_CPU

9/12 move HP_DET to GPIO53,
MIC_DET to GPIO51 (CS only)



<DB>Reserved RC127

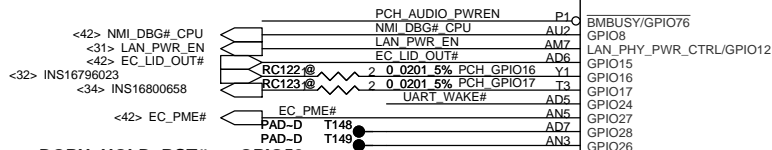
<DB>Update net name EDP_HP_D_R-->EDP_HP_D



PD on KBC page

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9/12 reserve EC_FB_CLAMP_TGL_REQ# on GPIO16
9/12 reserve DGPU_GC6_EN on GPIO17



9/12 reserve DGPU_HOLD_RST# on GPIO58

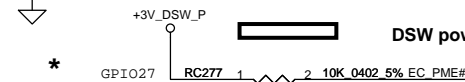
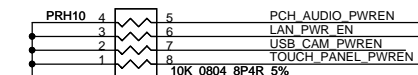
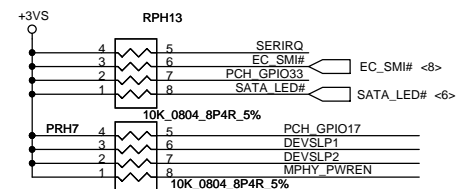
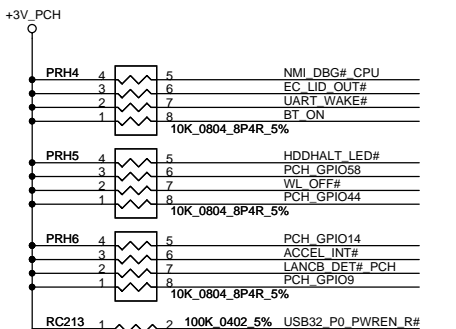
No sensor hub

No sensor hub

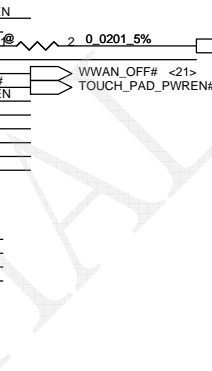
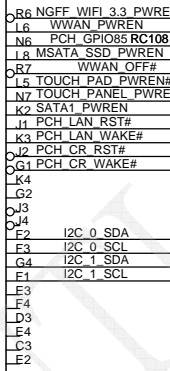
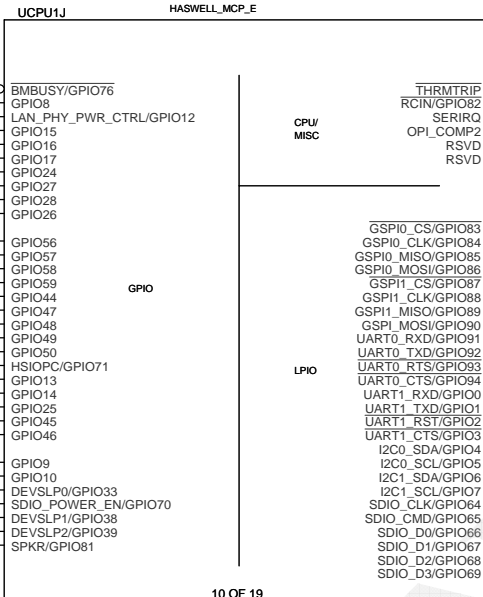
10/23 [SI] change ACCEL_INT# to GPIO46

<DB>CS only PCH_HP_DET#

PCH_GPIO81(HDA_SPKR):NonCS only



PCH_GPIO27 (Have internal Pull-High)
High: VCCVRM VR Enable
Low: VCCVRM VR Disable



DG V0.9 PCH_OPIRCOMP
Width=12mil,spacing=12mil
Max length=500mil

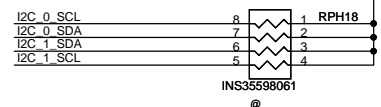
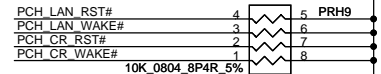
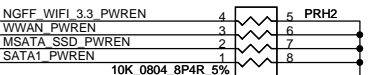
<DB>Add WWAN_PWREN

Boot BIOS Strap

PCH_GPIO86	Boot BIOS Location
0	SPI

9/12 reserve DGPU_PWR_EN on GPIO85

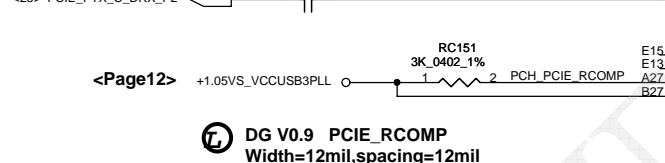
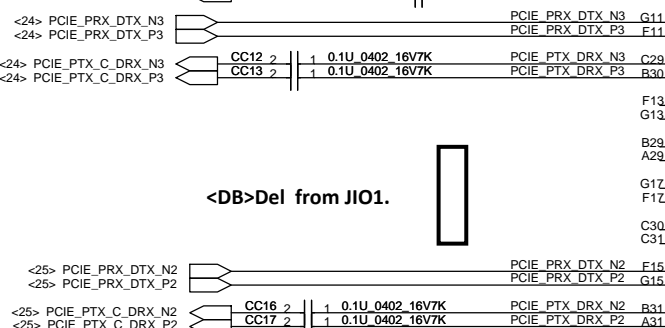
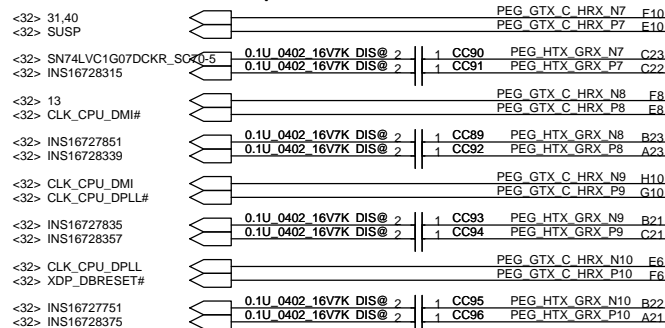
Add WWAN_OFF# on GPIO87



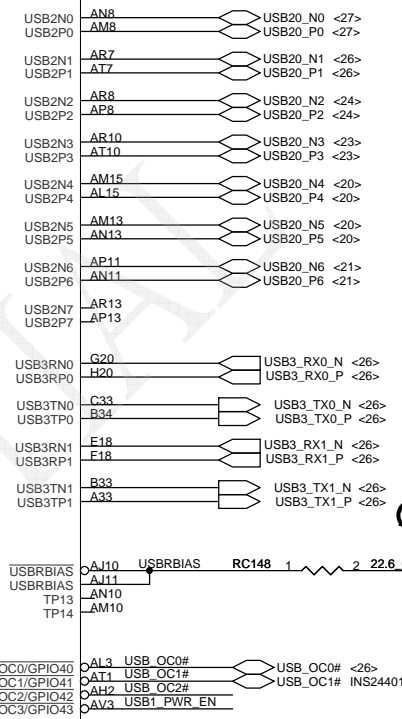
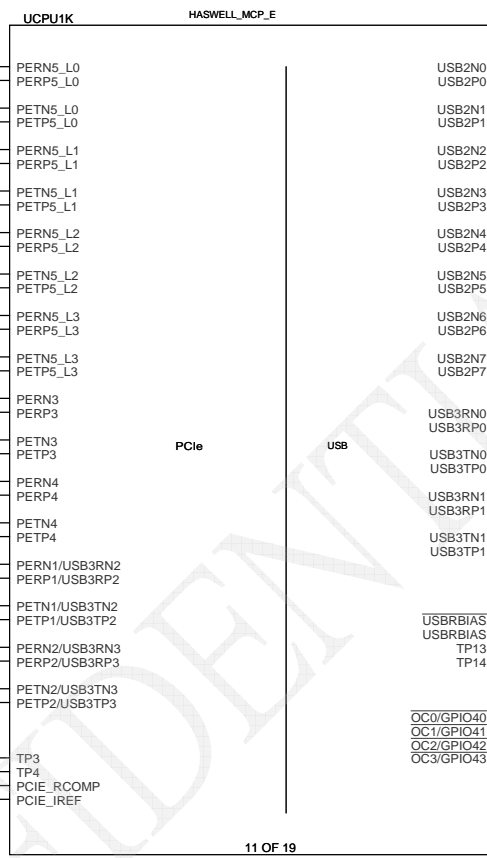
DSW power choose on page12

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Check with intel PEG which port




DG V0.9 PCIE_RCOMP
Width=12mil,spacing=12mil
Max length=500mil




USB2.0/USB3.0


USB2.0/USB3.0

USB2.0  <DB>IO board

WLAN/BT (NGFF)

Camera

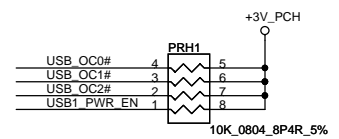
Touch screen  <DB>NonCS only

WWAN  <DB>Add WWAN

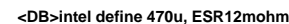
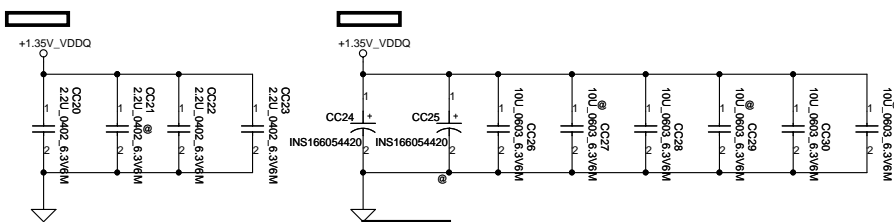
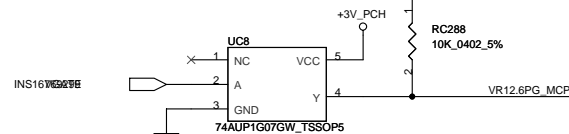
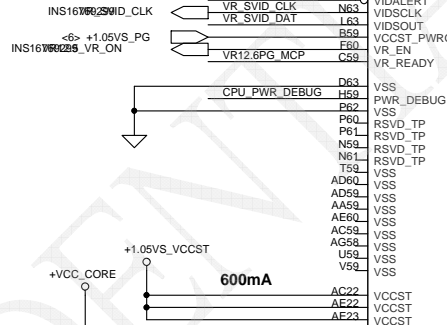
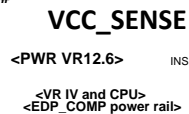
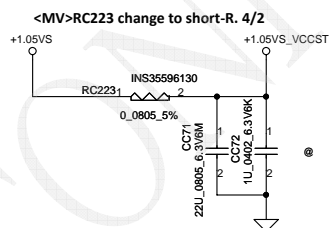
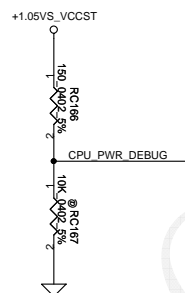
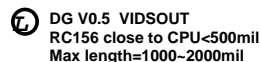
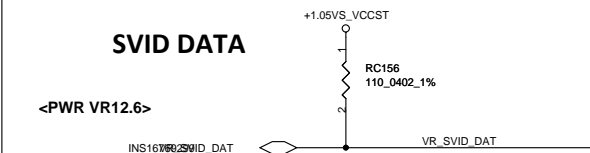
USB2.0/USB3.0

DG V0.9 USBRBIAS
Trace width=50ohm and spacing=15mil
Max length=500mil

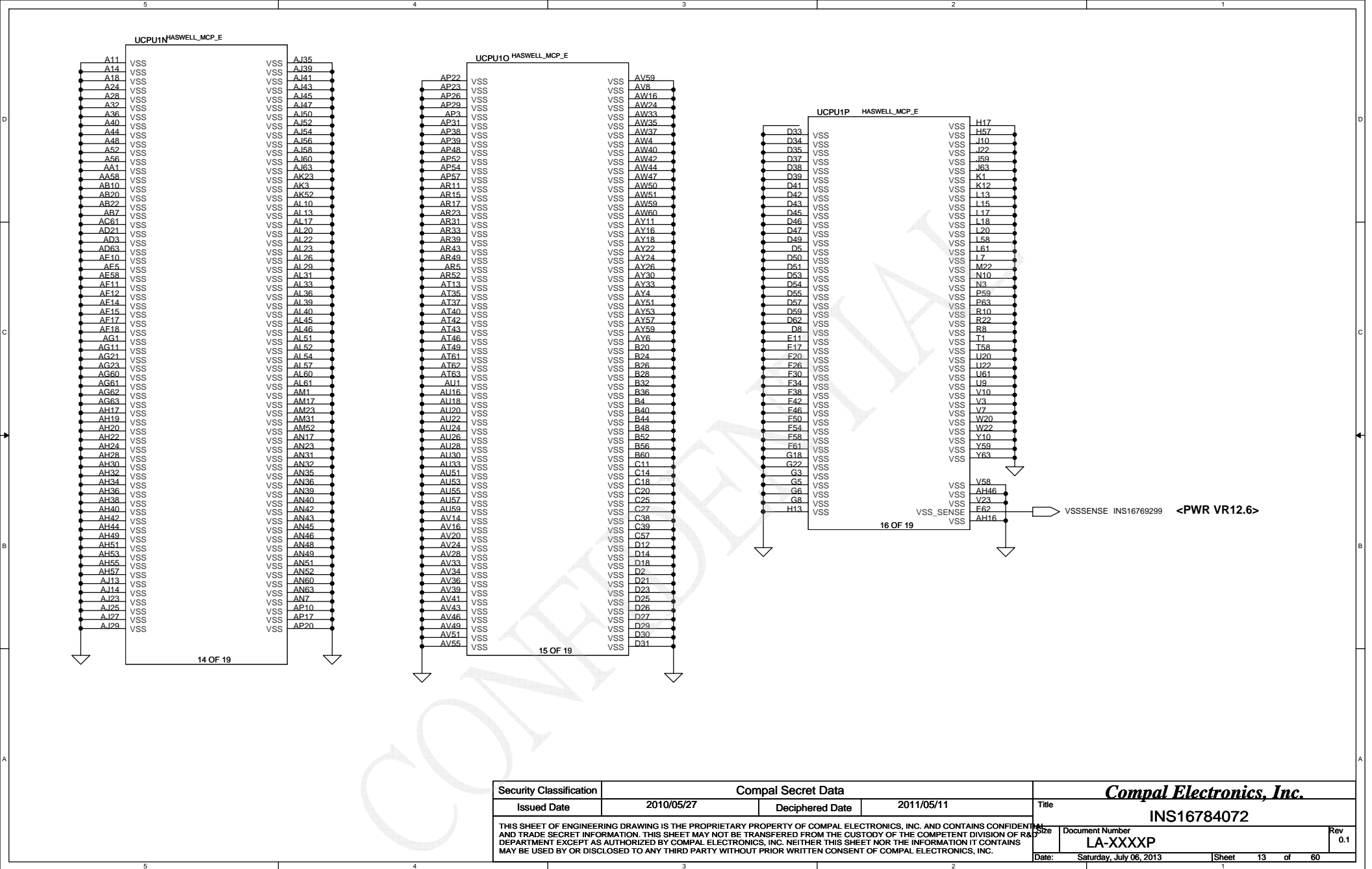
<DB>Remove RC149 and RC150 @ part



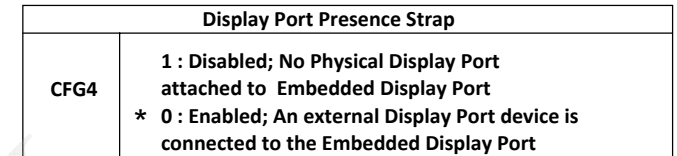
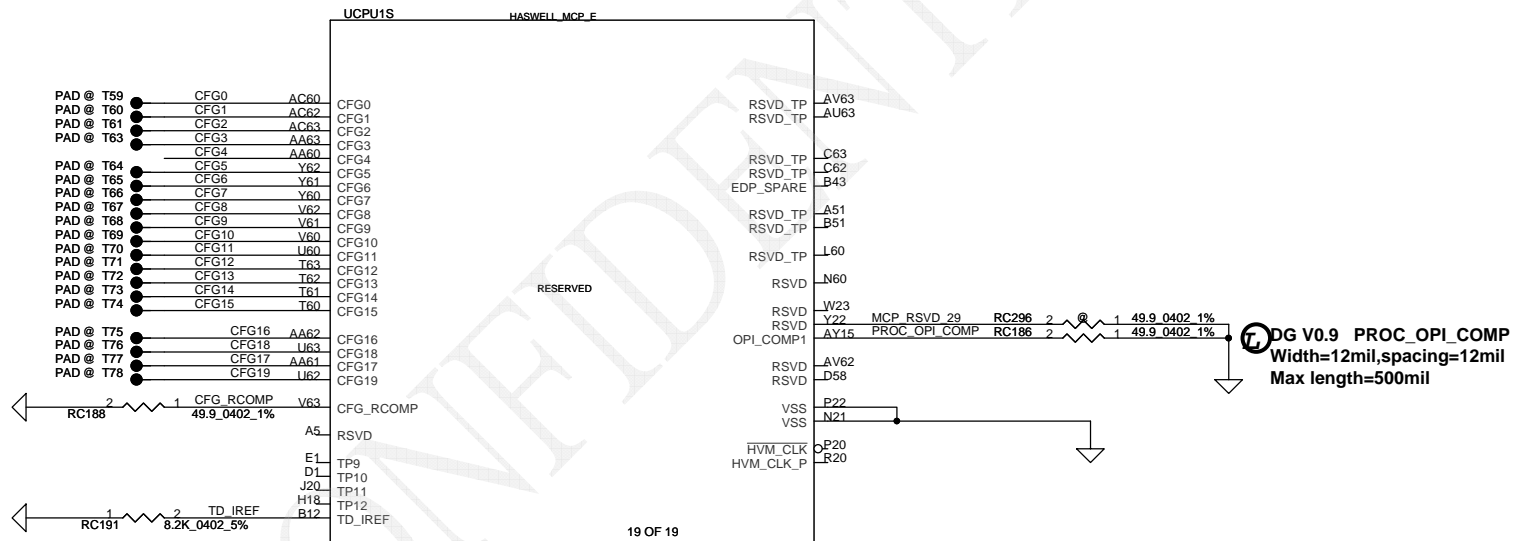
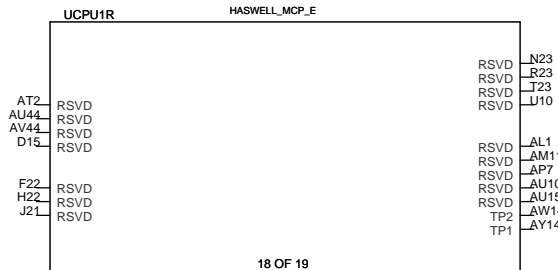
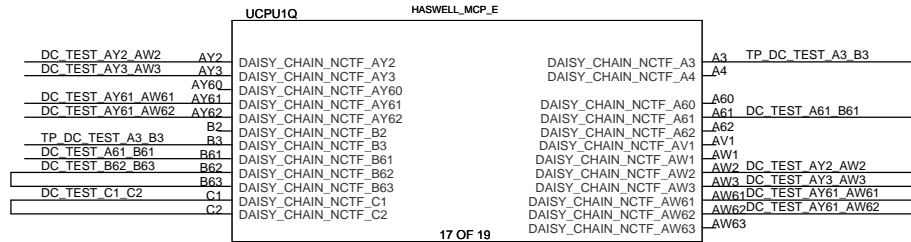
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Issued Date	2011/06/29	Deciphered Date	2011/06/29	PCIE,USB	
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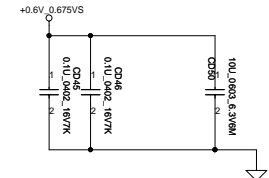
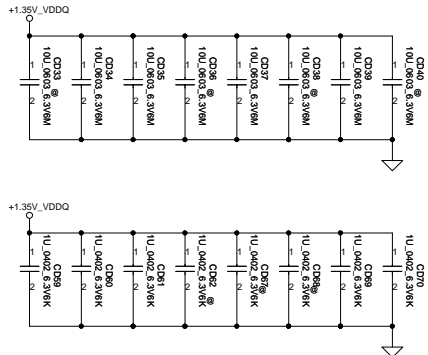
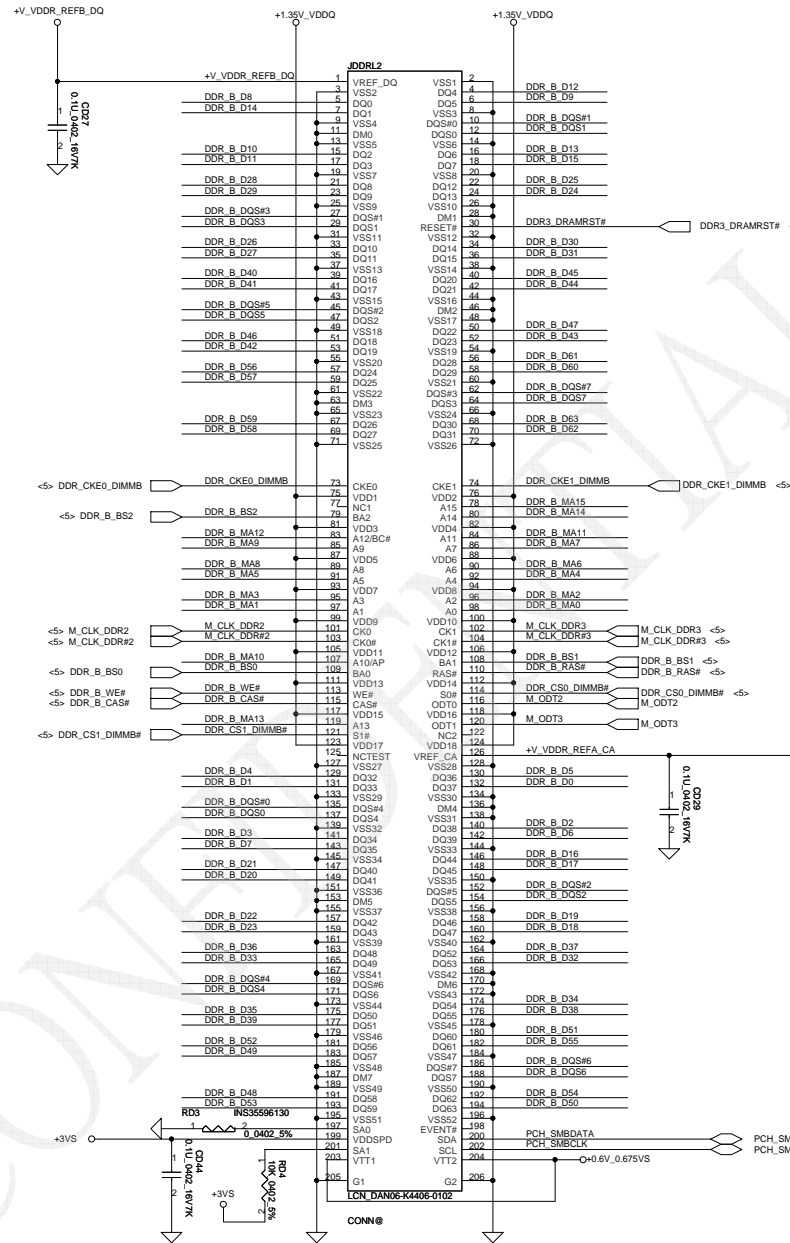


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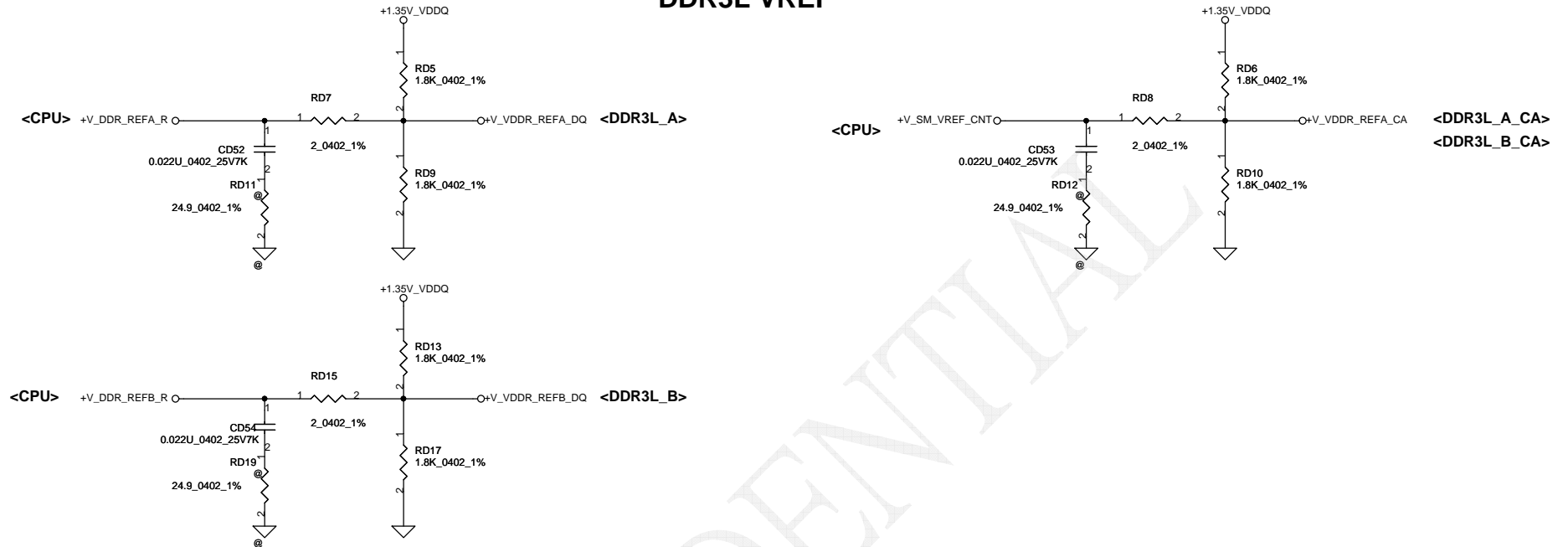
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/29	Deciphered Date	2011/06/29	Title	RSVD/CFG
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<5> DDR_B_D[0..63]
 <5> DDR_B_DQS[0..7]
 <5> DDR_B_DQS#0..7
 <5> DDR_B_MA[0..15]

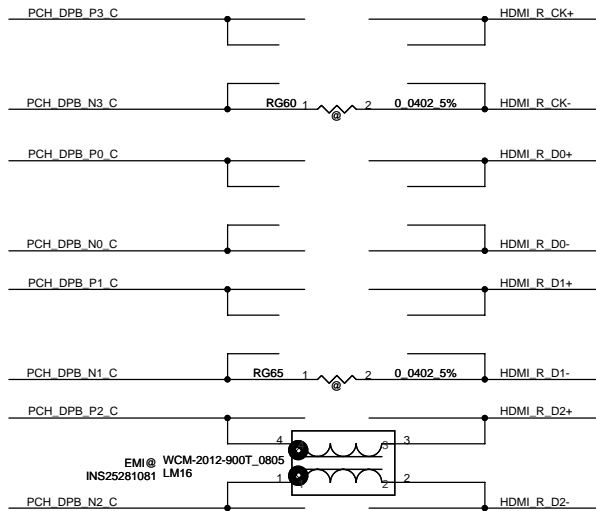
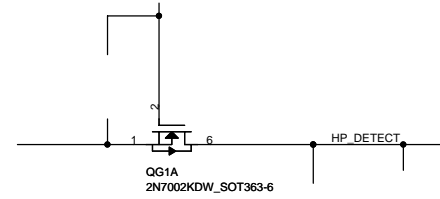
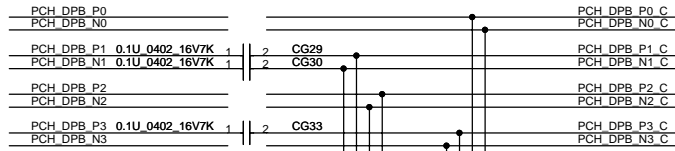


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						Size	Document Number			Rev		
						LA-XXXXP			0.1			
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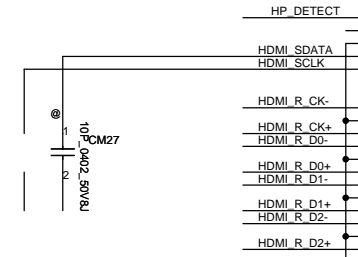
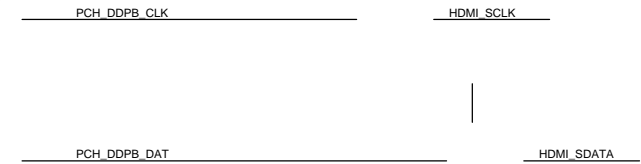
DDR3L VREF



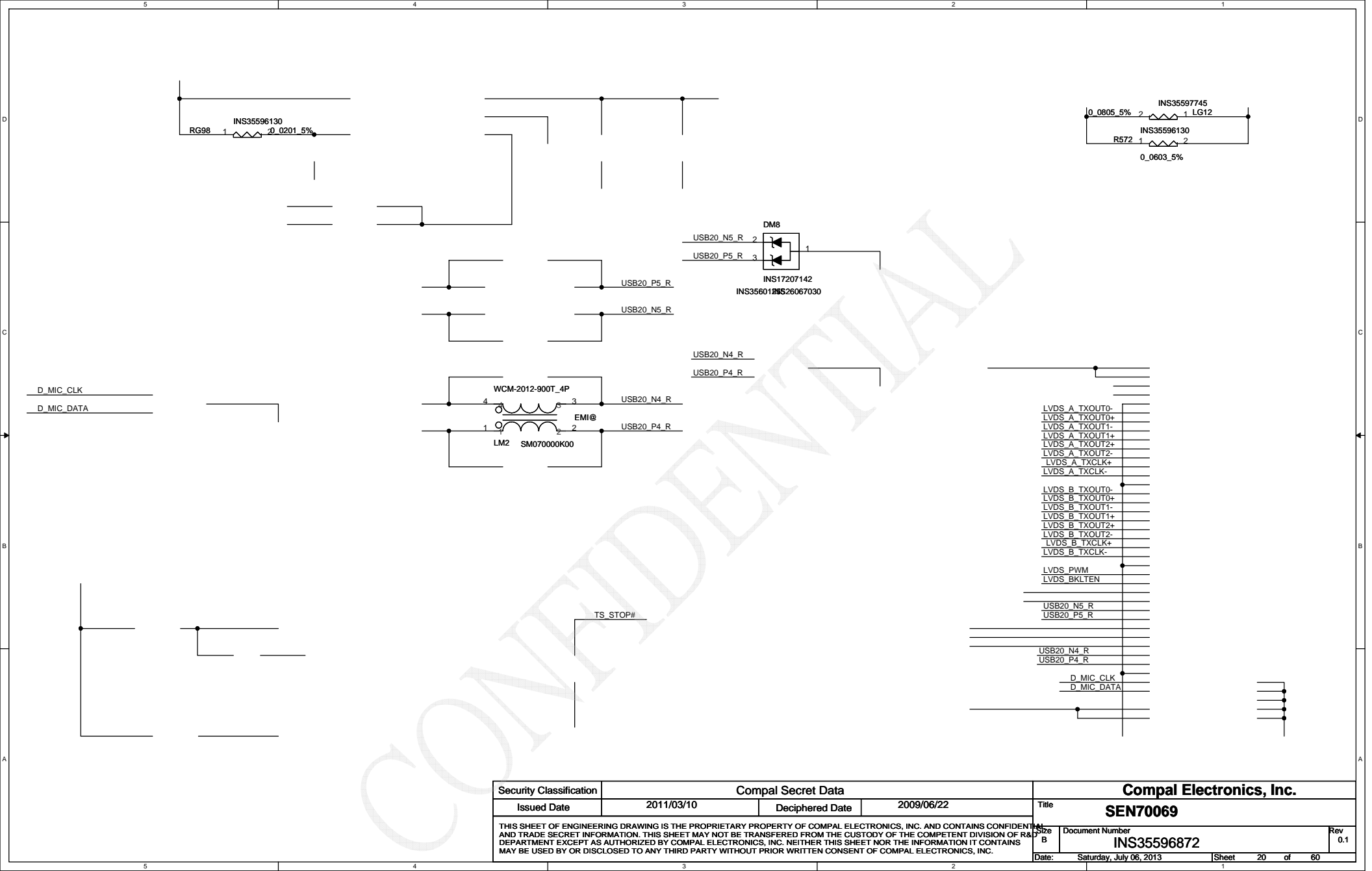
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Issued Date	2011/06/29	Deciphered Date	2011/06/29	Title	INS23886352
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				Date	Saturday, July 06, 2013
				Sheet	17 of 60
				Rev	0.1



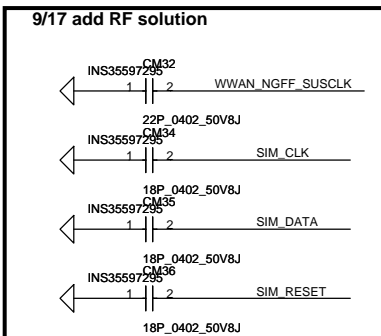
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HDMI_SCLK
PCH_DDPB_CLK
PCH_DDPB_DAT



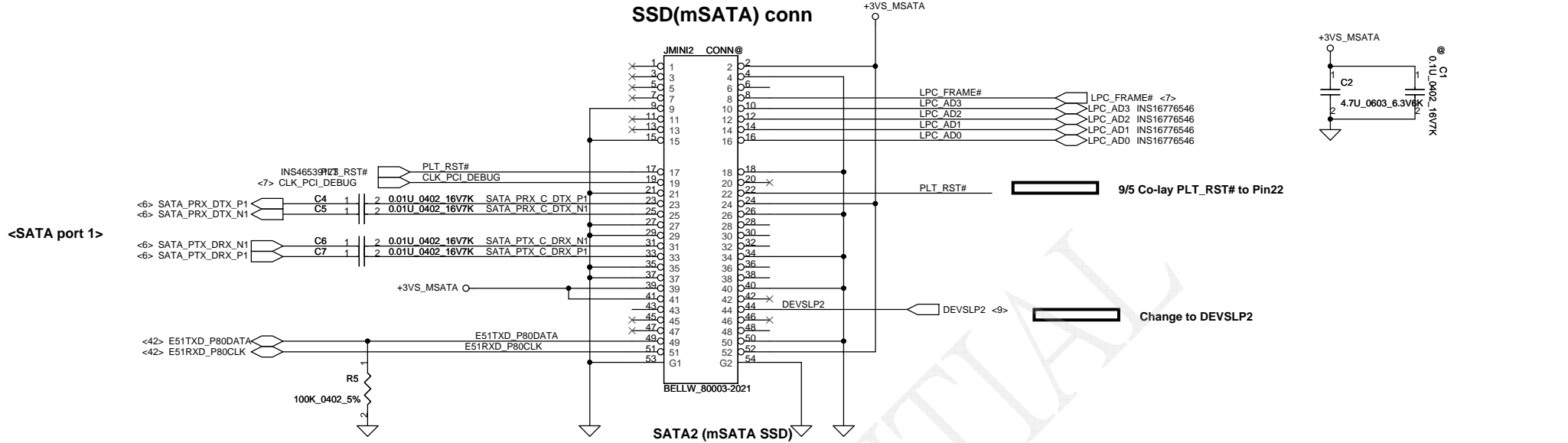
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Date: Saturday, July 06, 2013				Sheet 19 of 60	



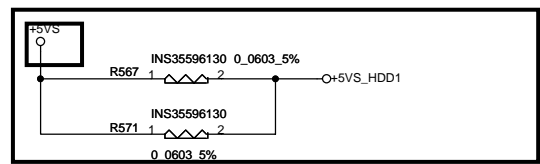
WWAN NGFF_B



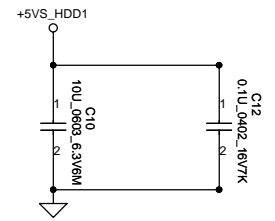
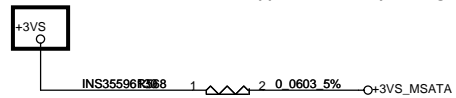
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Issued Date	2011/06/30	Deciphered Date	2013/06/30	Title	WWAN NGFF	
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				Date:	Saturday, July 06, 2013	Sheet 21 of 60



9/5 change Q23 +5VALW/+3VALW to +5VS/+3VS



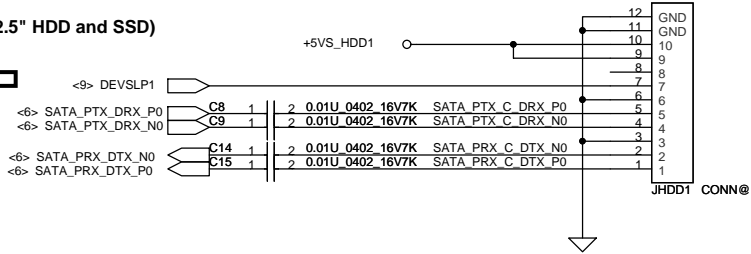
PV# 2013.01.24 Add R572 to support HDD heavy loading current .



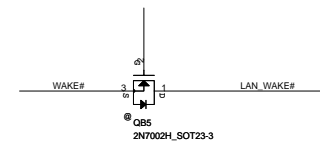
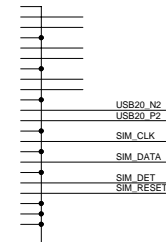
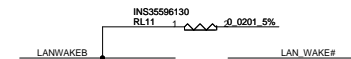
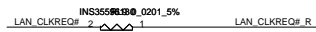
<DB>Change to 10 Pin **2.5" HDD Conn.**

SATA 1 (2.5" HDD and SSD)

<SATA port 0>



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				Date	Saturday, July 06, 2013
				Sheet	22 of 60



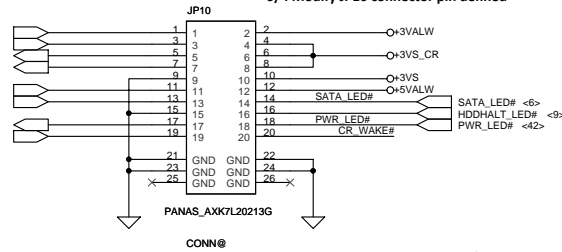
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Issued Date	2011/06/29	Deciphered Date	2011/0/03	Title	LAN 8111G
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Card reader conn

9/4 Modify JP10 connector pin defined

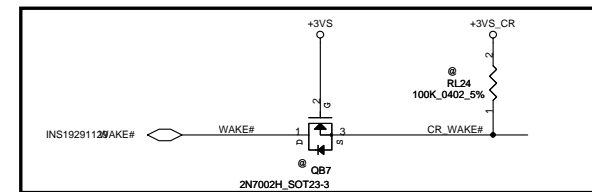
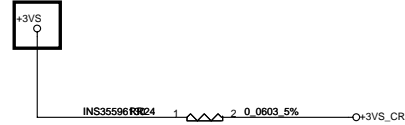
<10> PCIE_PTX_C_DRX_P2
<10> PCIE_PTX_C_DRX_N2
<10> PCIE_PRX_DTX_P2
<10> PCIE_PRX_DTX_N2
<7> CLK_PCIE_CR
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<7> CR_CLKREQ#
PLT_RST#

INS46539181



<CPU>

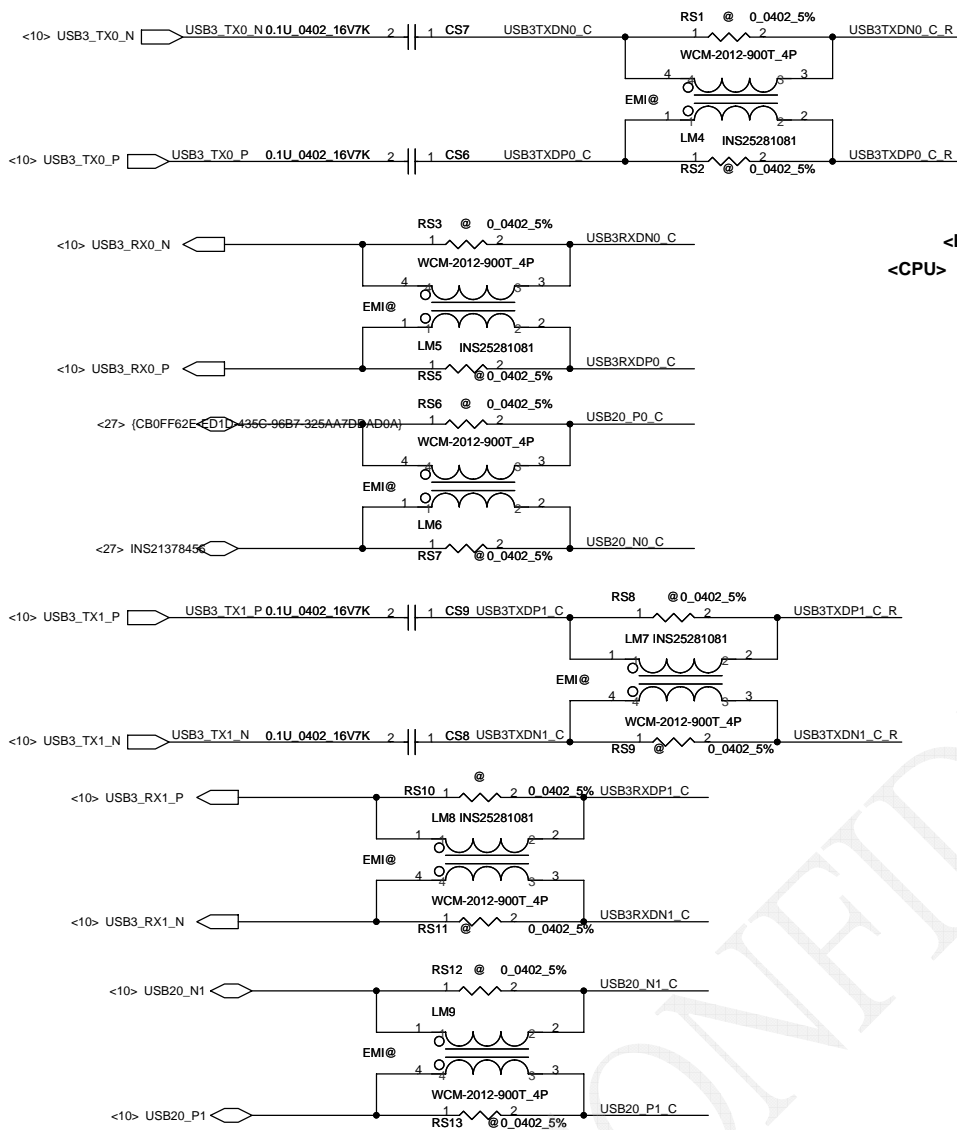
9/13 Change LAN power solution to USB charger power



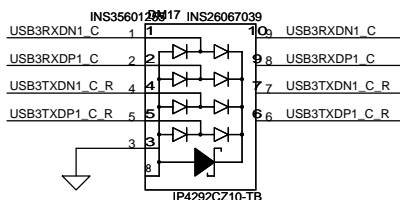
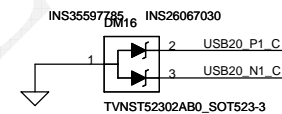
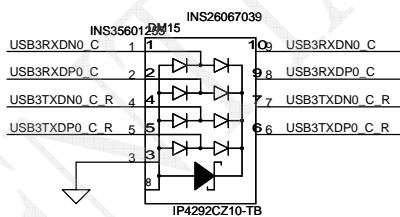
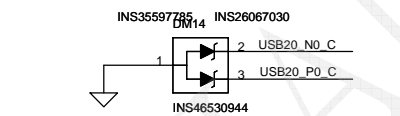
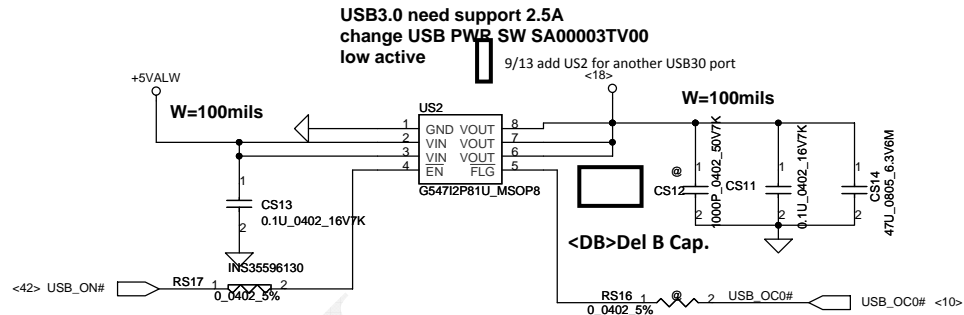
PV# 2013.01.23 Add QB7 abd RL24 to support OBFF

PV# 2013.02.22 Unpop QB4 and RL23 for not support OBFF

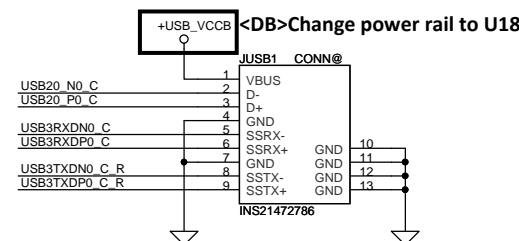
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				LA-XXXXP				Rev			
				Date				Sheet			
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<EC>
<CPU>

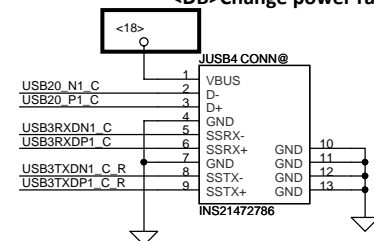


<DB>Update footprint to DC233008M10.
USB2.0/USB3.0 port 1



USB2.0/USB3.0 port 2

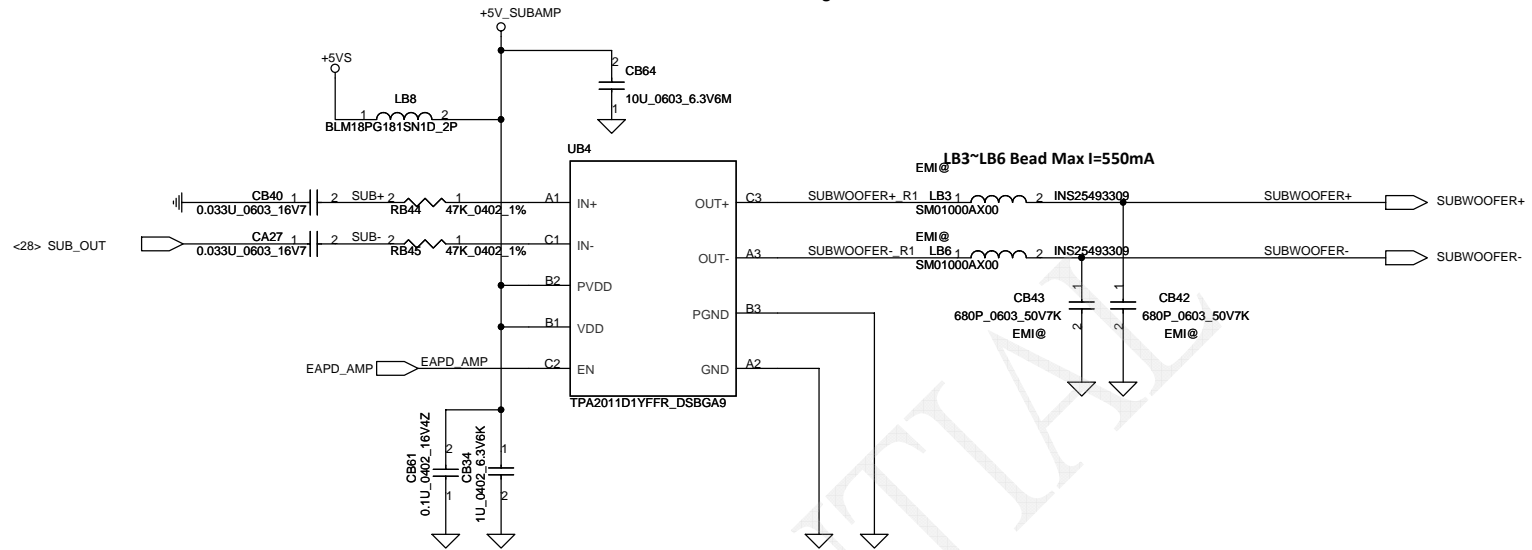
<DB>Change power rail to U18.



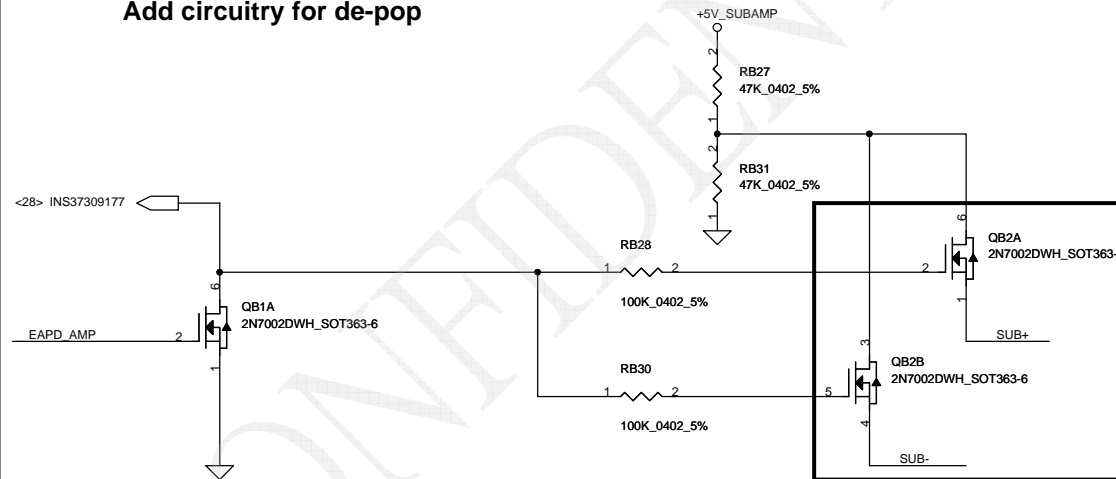
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Issued Date	2011/06/29	Deciphered Date	2011/06/29	Title	INS23886371
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Subwoofer AMP TPA2011 = HPA01086YFFR

DB> TPA2012 Change to TPA2011



Add circuitry for de-pop



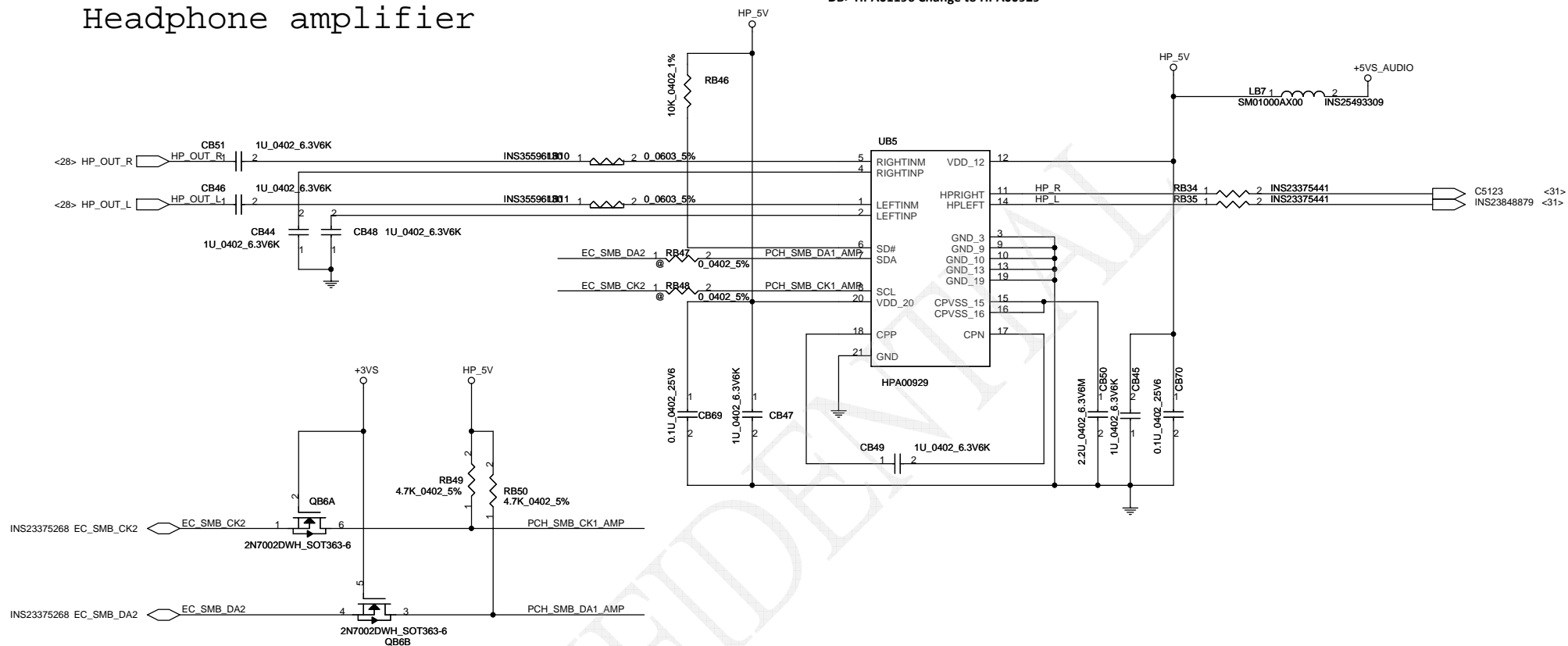
QB2 used Dual NPN BJT.
SB00000VH00 (METR3904KW)

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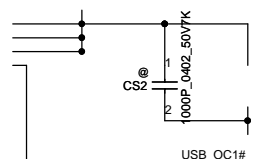
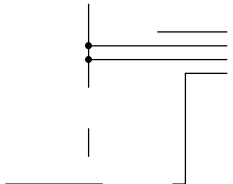
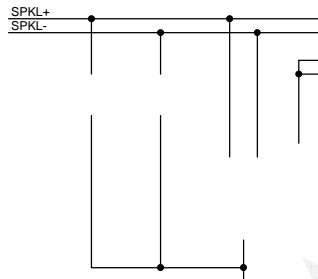
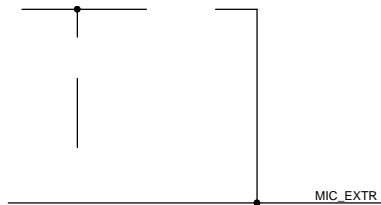
Headphone amplifier

Headphone AMP TPA6130A2 = HPA00929

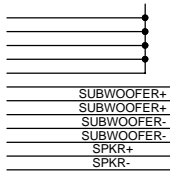
DB> HPA01196 Change to HPA00929



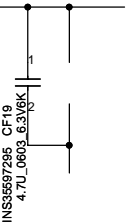
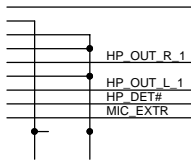
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								LA-XXXXP		0.1	
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EC LAN ISOLATEB#



EC LAN ISOLATEB#

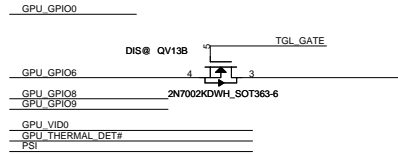
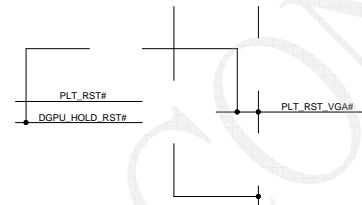


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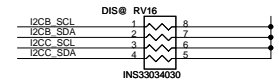
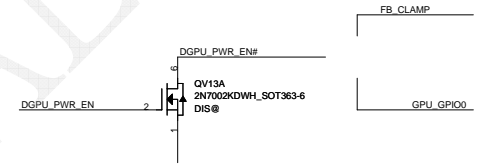
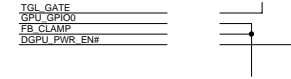
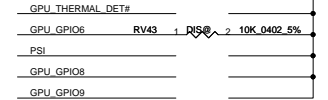
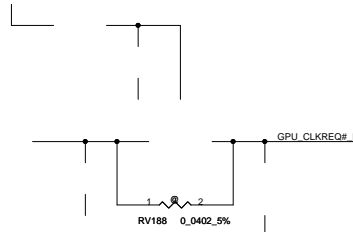
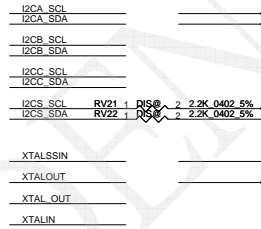
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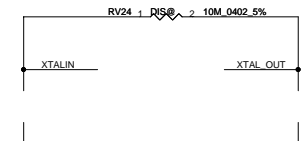
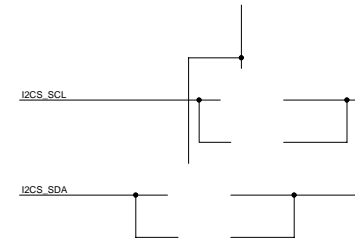
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TESTMODE



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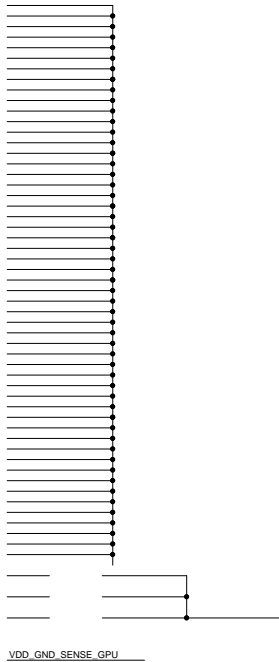


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STRAP0
STRAP1
STRAP2
STRAP3
STRAP4

ROM_CS
ROM_SCLK
ROM_SI
ROM_SO

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VDD_GND_SENSE_GPU

+1.5VGS_PWR_EN#

+1.5VGS_PWR_EN

CV75 DIS0
INS44533228

+1.5VGS_PWR_EN#

DGPU_PWROK
+1.5VGS_PWR_EN

DGPU_PWR_EN

DGPU_PWROK

+1.5VGS_PWR_EN#

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T1405
CLKA0
CLKA0P
CLKA1
CLKA1P

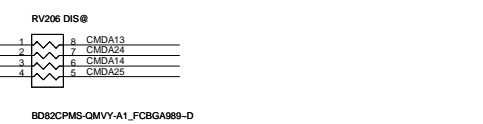
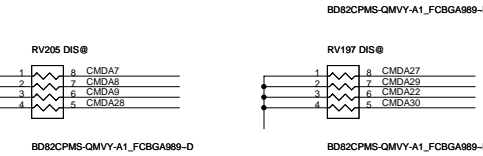
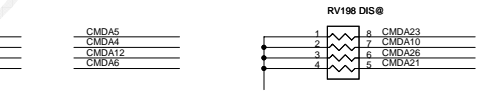
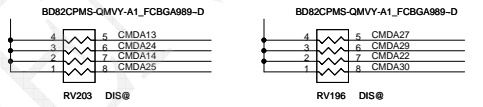
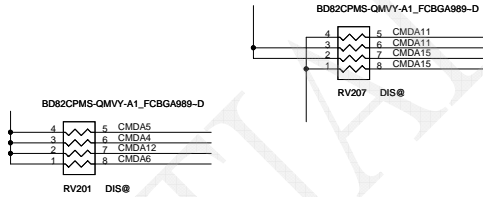
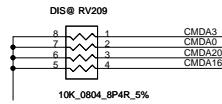
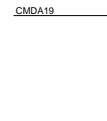
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CMDA17
CMDA18
CMDA19
CMDA20
CMDA21
CMDA22
CMDA23
CMDA24
CMDA25
CMDA26
CMDA27
CMDA28
CMDA29
CMDA30

DQMA0
DQMA1
DQMA2
DQMA3
DQMA4
DQMA5
DQMA6
DQMA7

DQSA#0
DQSA#1
DQSA#2
DQSA#3
DQSA#4
DQSA#5
DQSA#6
DQSA#7

DQSA0
DQSA1
DQSA2
DQSA3
DQSA4
DQSA5
DQSA6
DQSA7

RV137 1 2 2007/1/24 T-F 05:23:53
RV138 1 2 2007/1/24 T-F 05:23:53



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	INS16788846	Deciphered Date	WTPHEHW1CIS SYMBOL HW RELEASE 2002-0628.OLB	Rev 1.0
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			Date	Saturday, July 06, 2013
			Sheet	36 of 60

Memory Partition A RANK 0

Data0~Data31

MDA[0..63] AT24C16AN-10SU-2-7_S08-N
Manufacturer R8S>

Rank 0

Rank 0

Mode E - Mirror Mode Mapping

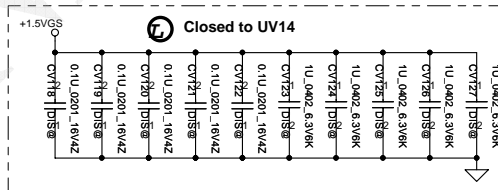
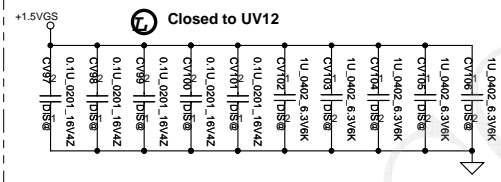
Address	RANK 0		RANK 1	
	0..31	32..63	0..31	32..63
FBx_CMD0	ODT_L		ODT_L	
FBx_CMD1			CS1#_L	
FBx_CMD2	CS0#_L			
FBx_CMD3	CKE_L		CKE_L	
FBx_CMD4	A9	A9	A11	A11
FBx_CMD5	A6	A6	A7	A7
FBx_CMD6	A3	A3	BA1	BA1
FBx_CMD7	A0	A0	A12	A12
FBx_CMD8	A8	A8	A8	A8
FBx_CMD9	A12	A12	A0	A0
FBx_CMD10	A1	A1	A2	A2
FBx_CMD11	RAS#	RAS#	RAS#	RAS#
FBx_CMD12	A13	A13	A14	A14
FBx_CMD13	BA1	BA1	A3	A3
FBx_CMD14	A14	A14	A13	A13
FBx_CMD15	CAS#	CAS#	CAS#	CAS#
FBx_CMD16		ODT_H		ODT_H
FBx_CMD17			CS1#_H	
FBx_CMD18		CS0#_H		
FBx_CMD19		CKE_H		CKE_H
FBx_CMD20	RST	RST	RST	RST
FBx_CMD21	A7	A7	A6	A6
FBx_CMD22	A4	A4	A5	A5
FBx_CMD23	A11	A11	A9	A9
FBx_CMD24	A2	A2	A1	A1
FBx_CMD25	A10	A10	WE#	WE#
FBx_CMD26	A5	A5	A4	A4
FBx_CMD27	BA2	BA2		
FBx_CMD28	WE#	WE#	A10	A10
FBx_CMD29	BA0	BA0	BA0	BA0
FBx_CMD30			BA2	BA2

Group 1

Group 0

Group 2

Group 3

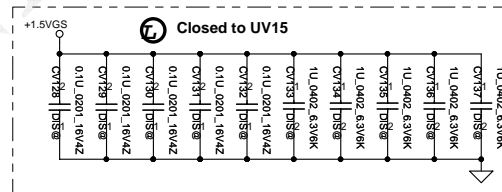
L:Group 1
H:Group 2L:Group 0
H:Group 3

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	INS16788846	Deciphered Date	WTPHWH1\ICIS SYMBOL\WHW RELEASE2002-0628.OLB	Rev	1.0
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Customer Document Number				INS35596872	
Date: Saturday, July 06, 2013				Sheet 37 of 60	

MDA[0..63] MAX14566EETA+_TDFN-EP8_2X2
Manufacturer Pcs>



	RANK 0		RANK 1	
Address	0..31	32..63	0..31	32..63
FBx_CMD0	ODT_L		ODT_L	
FBx_CMD1			CS1#_L	
FBx_CMD2	CS0#_L			
FBx_CMD3	CKE_L		CKE_L	
FBx_CMD4	A9	A9	A11	A11
FBx_CMD5	A6	A6	A7	A7
FBx_CMD6	A3	A3	BA1	BA1
FBx_CMD7	A0	A0	A12	A12
FBx_CMD8	A8	A8	A8	A8
FBx_CMD9	A12	A12	A0	A0
FBx_CMD10	A1	A1	A2	A2
FBx_CMD11	RAS#	RAS#	RAS#	RAS#
FBx_CMD12	A13	A13	A14	A14
FBx_CMD13	BA1	BA1	A3	A3
FBx_CMD14	A14	A14	A13	A13
FBx_CMD15	CAS#	CAS#	CAS#	CAS#
FBx_CMD16		ODT_H		ODT_H
FBx_CMD17				CS1#_H
FBx_CMD18		CS0#_H		
FBx_CMD19		CKE_H		CKE_H
FBx_CMD20	RST	RST	RST	RST
FBx_CMD21	A7	A7	A6	A6
FBx_CMD22	A4	A4	A5	A5
FBx_CMD23	A11	A11	A9	A9
FBx_CMD24	A2	A2	A1	A1
FBx_CMD25	A10	A10	WE#	WE#
FBx_CMD26	A5	A5	A4	A4
FBx_CMD27	BA2	BA2		
FBx_CMD28	WE#	WE#	A10	A10
FBx_CMD29	BA0	BA0	BA0	BA0
FBx_CMD30			BA2	BA2



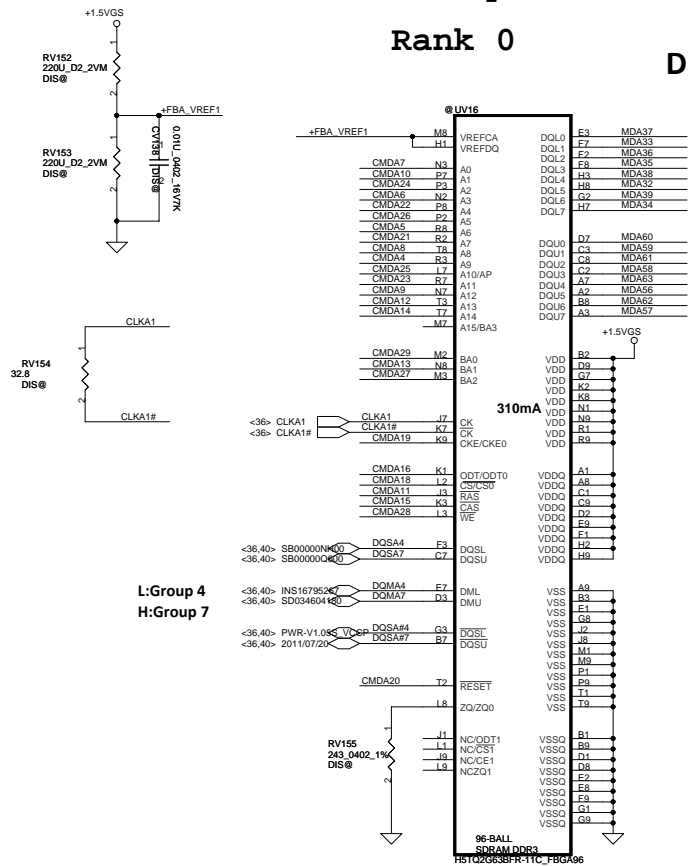
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Issued Date	INS16788846	Deciphered Date	ITPEHW1CIS SYMBOL	UNCLASSIFIED 2002-0628.OLB		
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				INS47268605	1.0	
Date: Saturday, July 06, 2013				Sheet	38	of 60

Memory Partition A RANK 0 32 bits

Rank 0

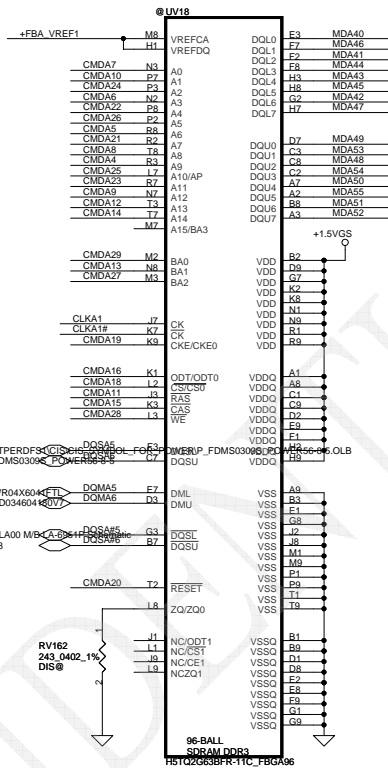
Data32~Data63

Rank 0

MDA[0..63] INS23313965
Manufacturer P06>L:Group 4
H:Group 7

Group 4

Group 7

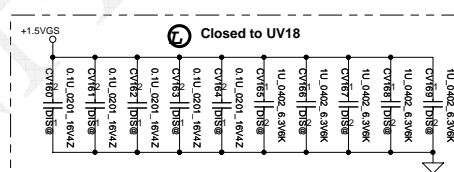
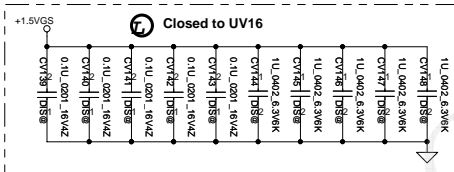
L:Group 5
H:Group 6

Group 5

Group 6

Mode E - Mirror Mode Mapping

Address	RANK 0		RANK 1	
	0..31	32..63	0..31	32..63
FBx_CMD0	ODT_L		ODT_L	
FBx_CMD1			CSI#_L	
FBx_CMD2	CS0#_L			
FBx_CMD3	CKE_L		CKE_L	
FBx_CMD4	A9	A9	A11	A11
FBx_CMD5	A6	A6	A7	A7
FBx_CMD6	A3	A3	BA1	BA1
FBx_CMD7	A0	A0	A12	A12
FBx_CMD8	A8	A8	A8	A8
FBx_CMD9	A12	A12	A0	A0
FBx_CMD10	A1	A1	A2	A2
FBx_CMD11	RAS#	RAS#	RAS#	RAS#
FBx_CMD12	A13	A13	A14	A14
FBx_CMD13	BA1	BA1	A3	A3
FBx_CMD14	A14	A14	A13	A13
FBx_CMD15	CAS#	CAS#	CAS#	CAS#
FBx_CMD16		ODT_H		ODT_H
FBx_CMD17			CSI#_H	
FBx_CMD18		CS0#_H		
FBx_CMD19		CKE_H		CKE_H
FBx_CMD20	RST	RST	RST	RST
FBx_CMD21	A7	A7	A6	A6
FBx_CMD22	A4	A4	A5	A5
FBx_CMD23	A11	A11	A9	A9
FBx_CMD24	A2	A2	A1	A1
FBx_CMD25	A10	A10	WE#	WE#
FBx_CMD26	A5	A5	A4	A4
FBx_CMD27	BA2	BA2		
FBx_CMD28	WE#	WE#	A10	A10
FBx_CMD29	BA0	BA0	BA0	BA0
FBx_CMD30			BA2	BA2

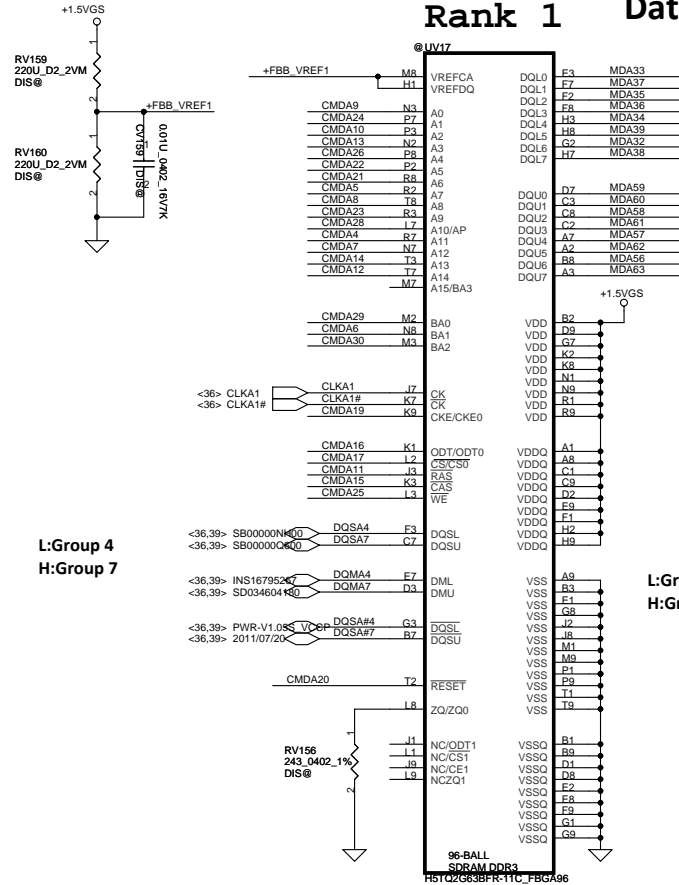


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Date: Saturday, July 06, 2013					Sheet 39 of 60

Memory Partition A RANK 1 32 bits

Rank 1 Data32~Data63

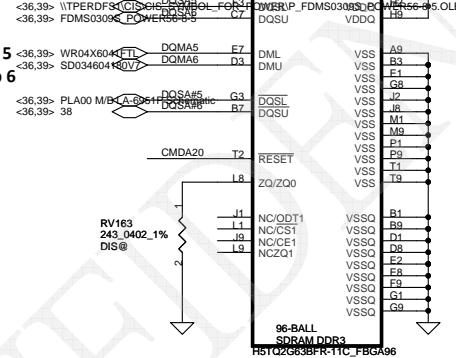
Rank 1



Group 4

Group 7

L:Group 5
H:Group 6

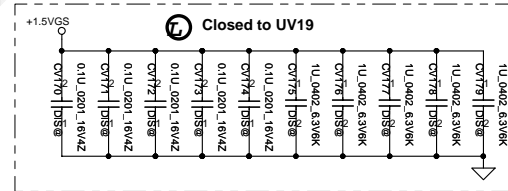
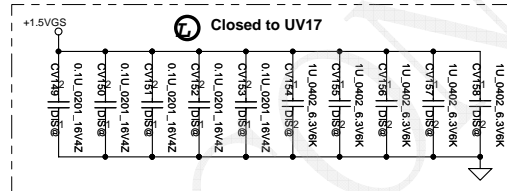


Group 5

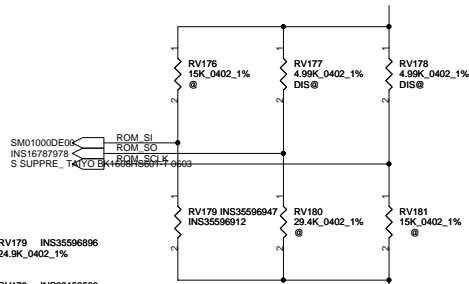
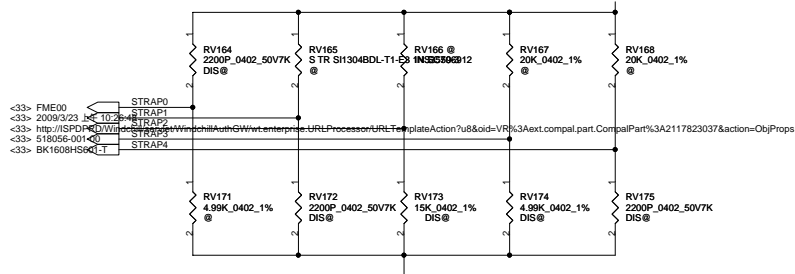
Group 6

Mode E - Mirror Mode Mapping

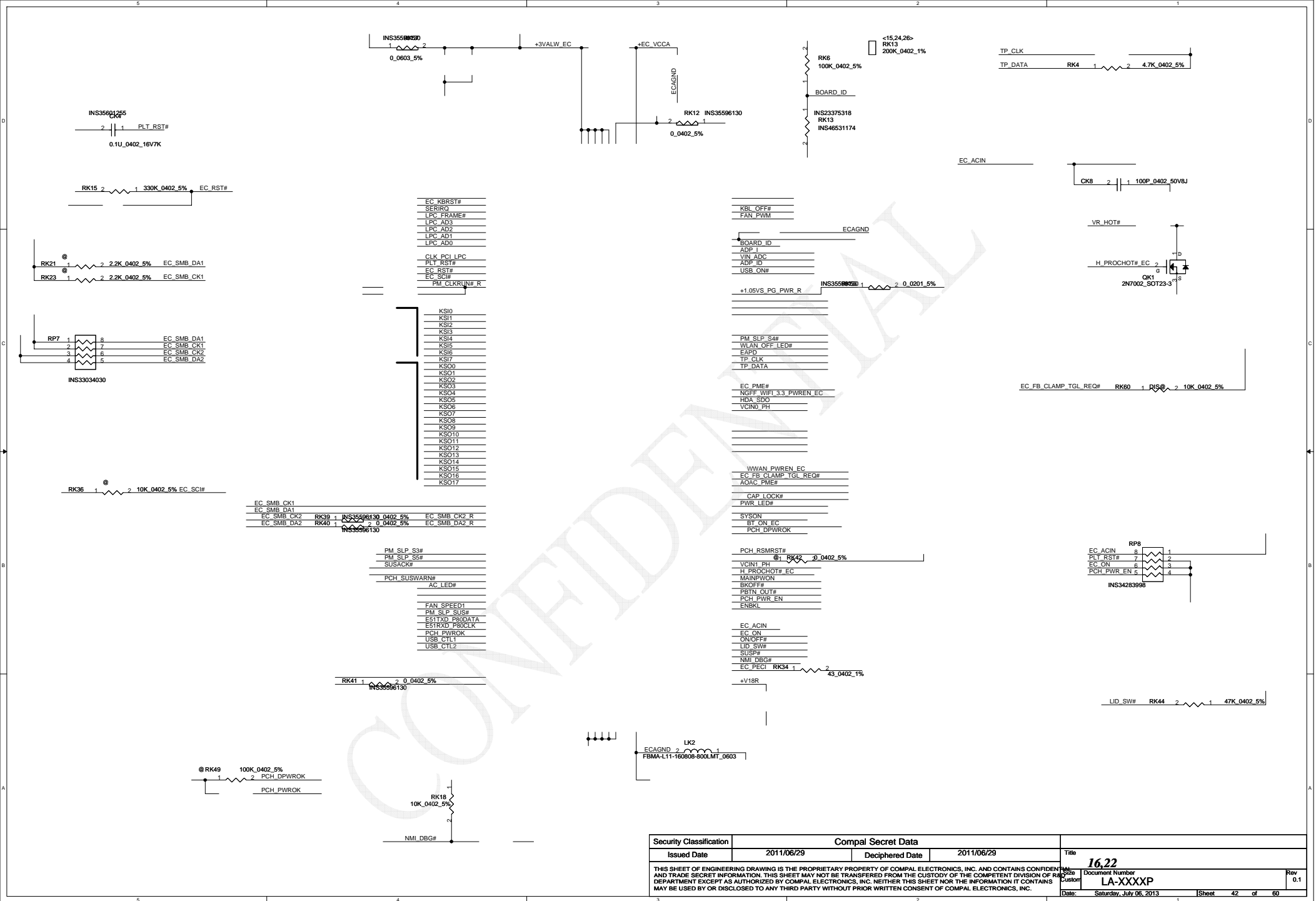
Address	RANK 0	RANK 1
FBx_CMD0	0..31	32..63
FBx_CMD1	ODT_L	ODT_L
FBx_CMD2	CS0#_L	CS1#_L
FBx_CMD3	CKE_L	CKE_L
FBx_CMD4	A9	A11
FBx_CMD5	A6	A7
FBx_CMD6	A3	BA1
FBx_CMD7	A0	A12
FBx_CMD8	A8	A8
FBx_CMD9	A12	A0
FBx_CMD10	A1	A2
FBx_CMD11	RAS#	RAS#
FBx_CMD12	A13	A14
FBx_CMD13	BA1	A3
FBx_CMD14	A14	A13
FBx_CMD15	CAS#	CAS#
FBx_CMD16	ODT_H	ODT_H
FBx_CMD17		CS1#_H
FBx_CMD18	CS0#_H	
FBx_CMD19	CKE_H	CKE_H
FBx_CMD20	RST	RST
FBx_CMD21	A7	A6
FBx_CMD22	A4	A5
FBx_CMD23	A11	A9
FBx_CMD24	A2	A1
FBx_CMD25	A10	WE#
FBx_CMD26	A5	A4
FBx_CMD27	BA2	BA2
FBx_CMD28	WE#	A10
FBx_CMD29	BA0	BA0
FBx_CMD30		BA2

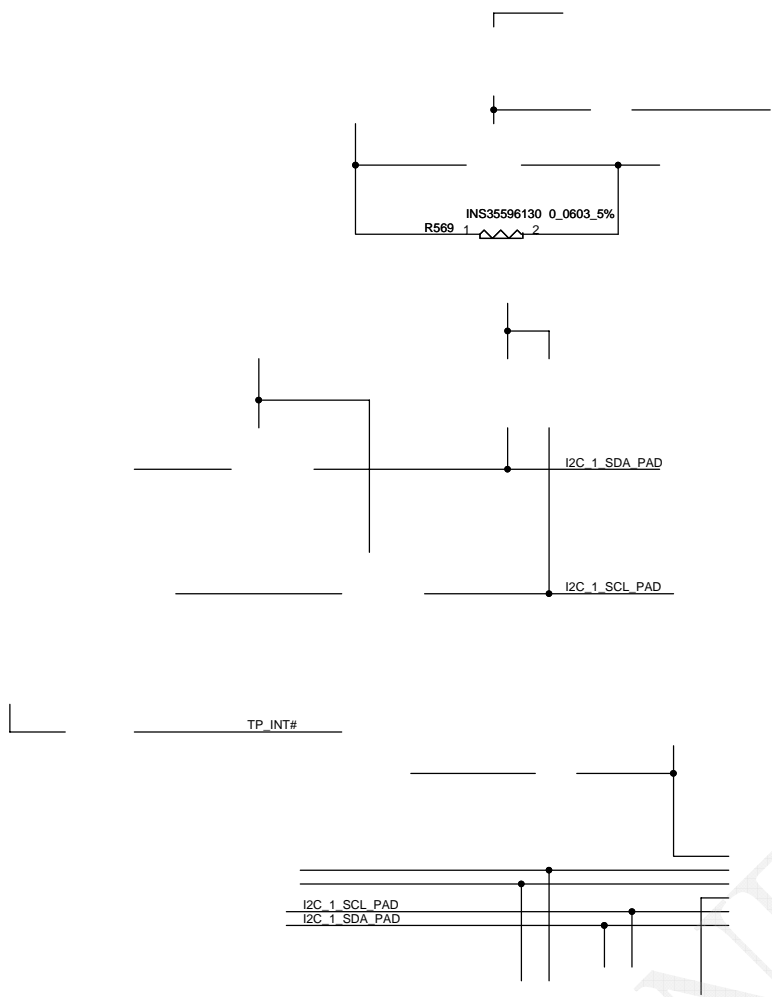


Security Classification	Compal Secret Data			Compal Electronics, Inc.	
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Date: Saturday, July 06, 2013		Sheet 41 of 60		INS35596872





KSI7			
KSI6			
KSI5			
KSI4			
KSI3			
KSI2			
KSI1			
KSI0			
KSO17			
KSO16			
KSO15			
KSO14			
KSO13			
KSO12			
KSO11			
KSO10			
KSO9			
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KSO1			
KSO0			

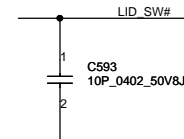
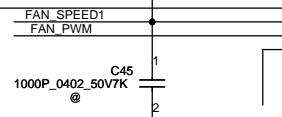
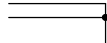
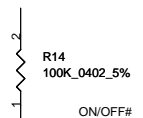
KSI1	1	
KSI7	2	
KSI6	3	
KSO9	4	
KSI4	5	
KSI5	6	
KSO0	7	
KSI2	8	
KSI3	9	
KSO5	10	
KSO1	11	
KSI0	12	
KSO2	13	
KSO4	14	
KSO7	15	
KSO8	16	
KSO6	17	
KSO3	18	
KSO12	19	
KSO13	20	
KSO14	21	
KSO11	22	
KSO10	23	
KSO15	24	
KSO16	25	
KSO17	26	
WL WHITE	27	
WLAN AMBER	28	
MUTE LED	29	
	30	
	31	
	32	

33	GND
34	GND

R7 1 2 360.0402 5%

ACES_51503-03241-001

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2011/06/29	Deciphered Date	2011/06/29	Title	C:\ORCADWIN\CAPTURE\LIBRARY\COMPAL
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				Date	Saturday, July 06, 2013
				Sheet	43 of 60

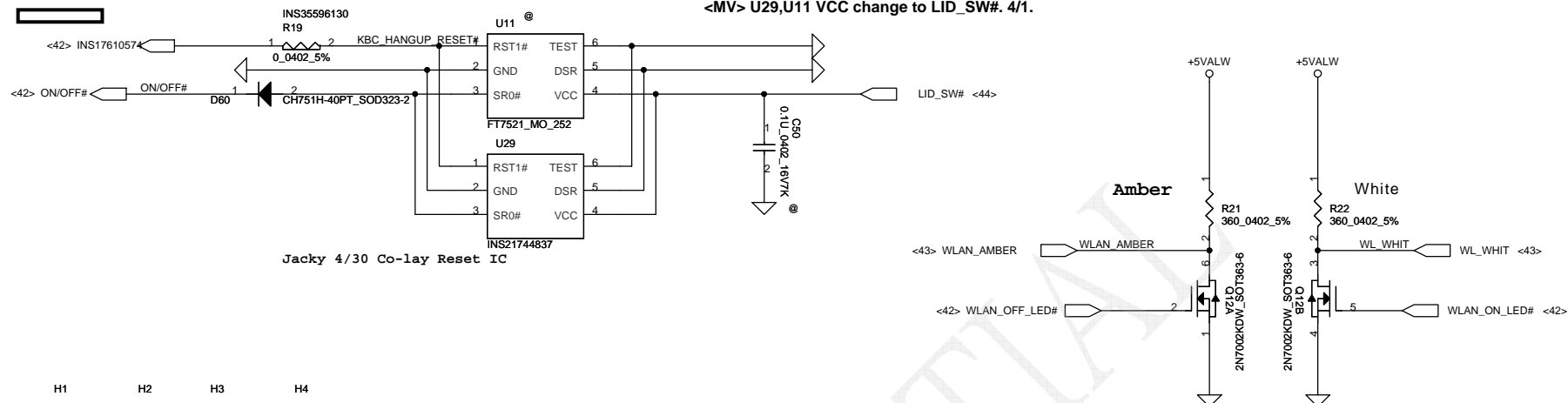


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				Date: Saturday, July 06, 2013	Rev 0.1
				Sheet 44 of 60	

<DB> RST1# connect to EN_3V_5V
<MV> RST1# change to EC_RST#,4/10

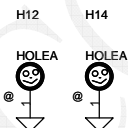
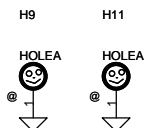
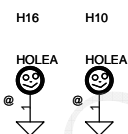
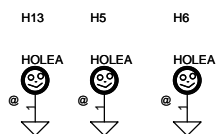
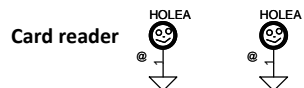
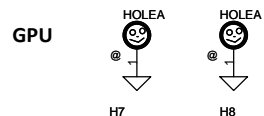
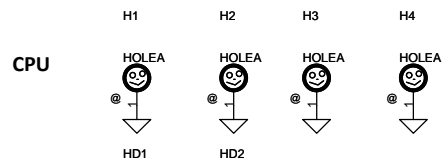
Reset IC

<MV> U29,U11 VCC change to LID_SW#. 4/1.



Jacky 4/30 Co-lay Reset IC

WLAN_OFF_LED# & WLAN_ON_LED# must keep low(0V) when lamp all off.



PCB

ZZZ3

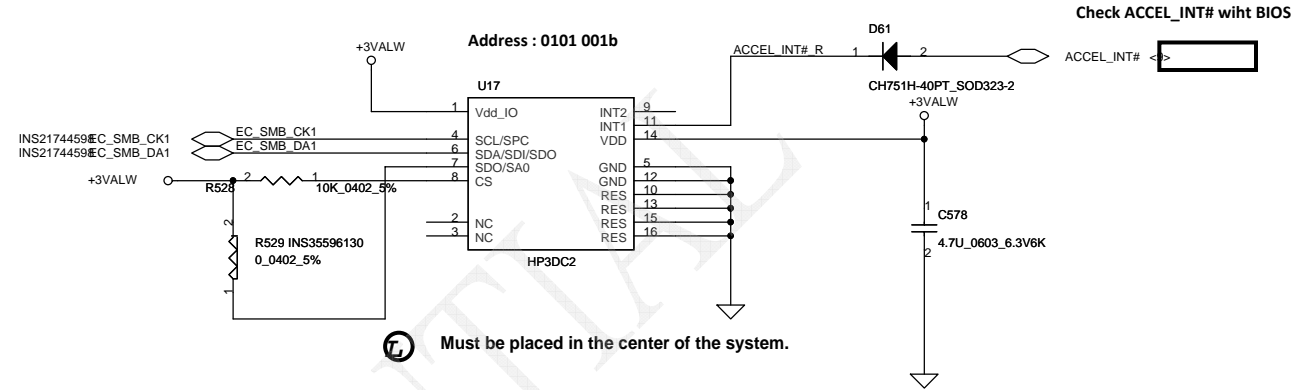
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INS35596872
INS47267858

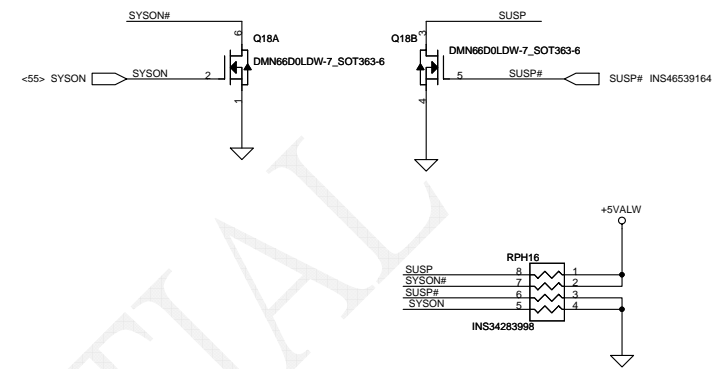
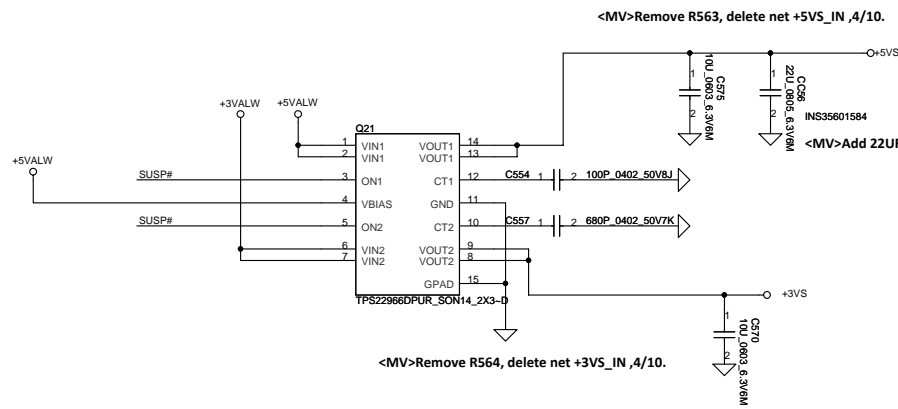
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
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ACCELEROMETER

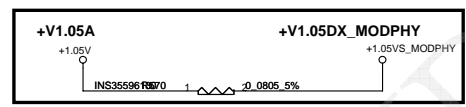
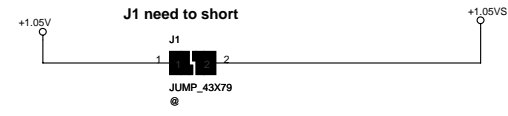
Sensor hub -->G-sensor



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+1.05V TO +1.05VS



AO4430L
VGS Max=+/- 20V
VGS(Th) max=2.5V

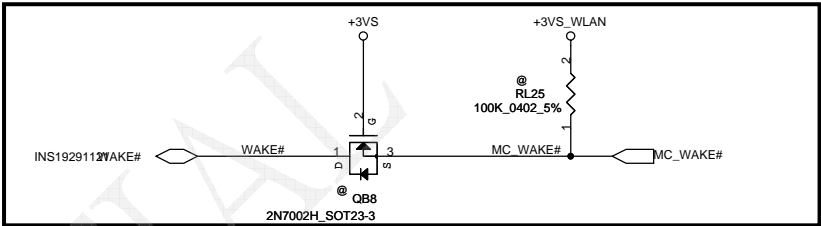
Rds Max=5.5m @VGS=10V
Rds Max=7.5m @VGS=4.5V

CPU +V1.05DX_MODPHY
Max Rdson <6m ohm
1840mA

AO4430L
VGS Max=+/- 20V
VGS(Th) max=2.5V
Rds Max=5.5m @VGS=10V
Rds Max=7.5m @VGS=4.5V

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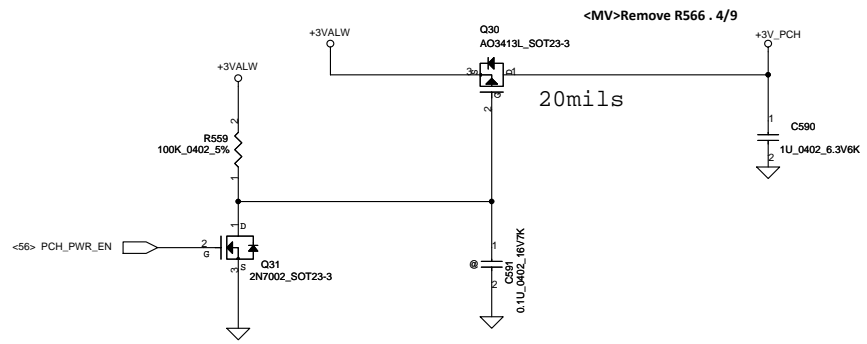
NGFF and WLAN



PV# 2013.01.23 Add Q8 abd RL25 to support OBFF

PV# 2013.02.22 Unpop Q8 and RL23 for not support OBFF

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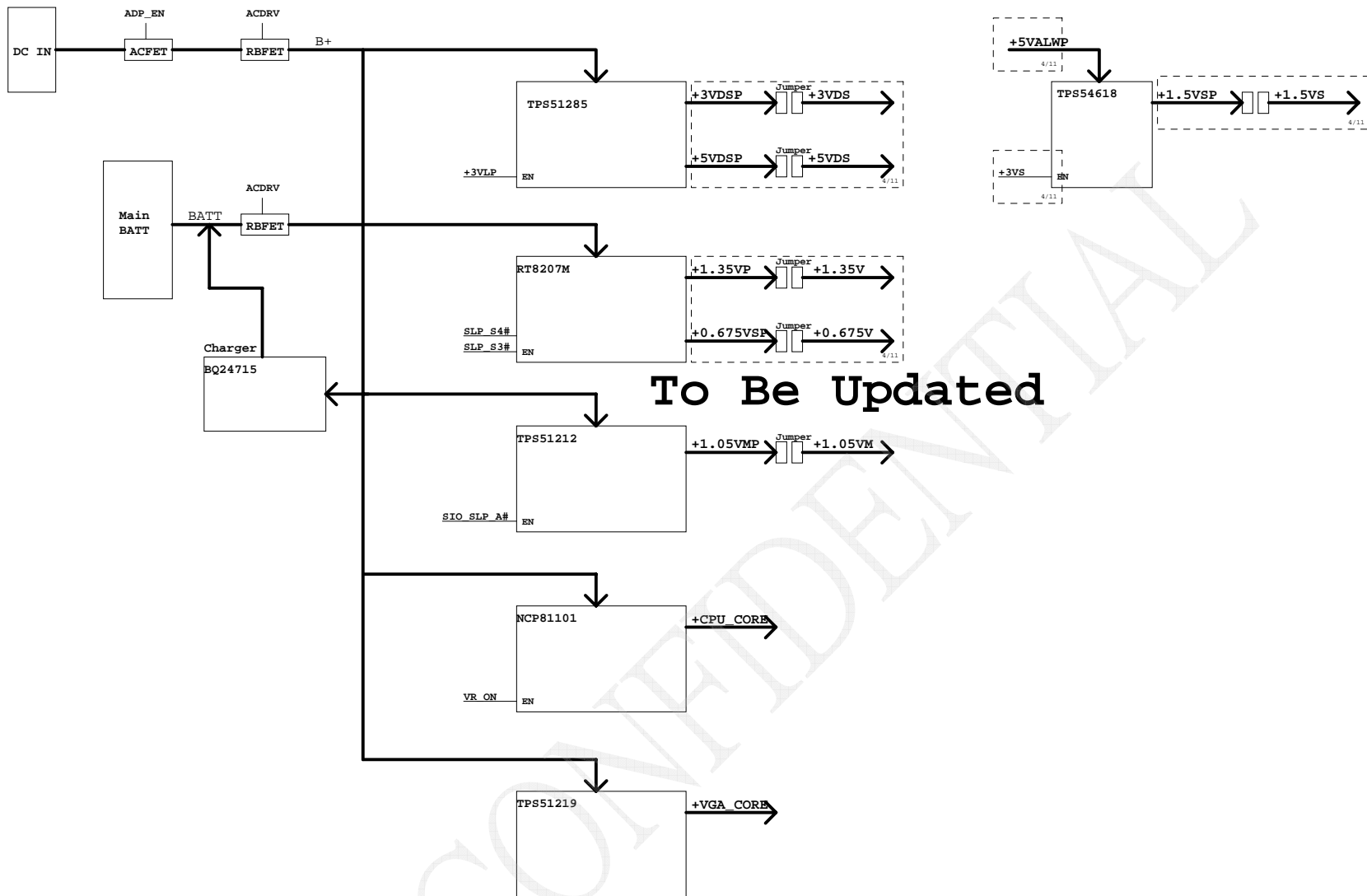


<DB>NonCS no +1.8VS and low switch-->PMOS

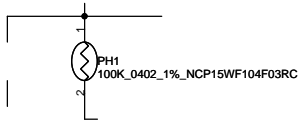
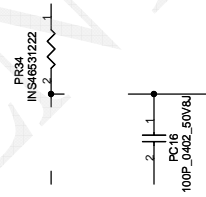
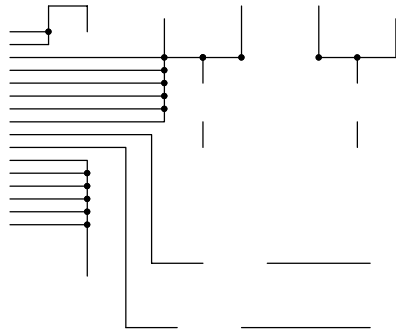
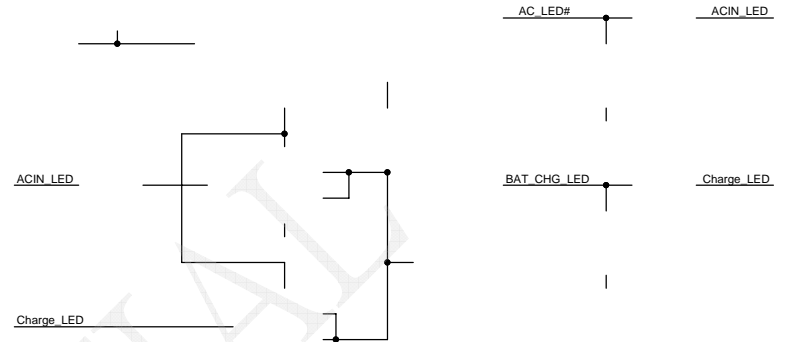
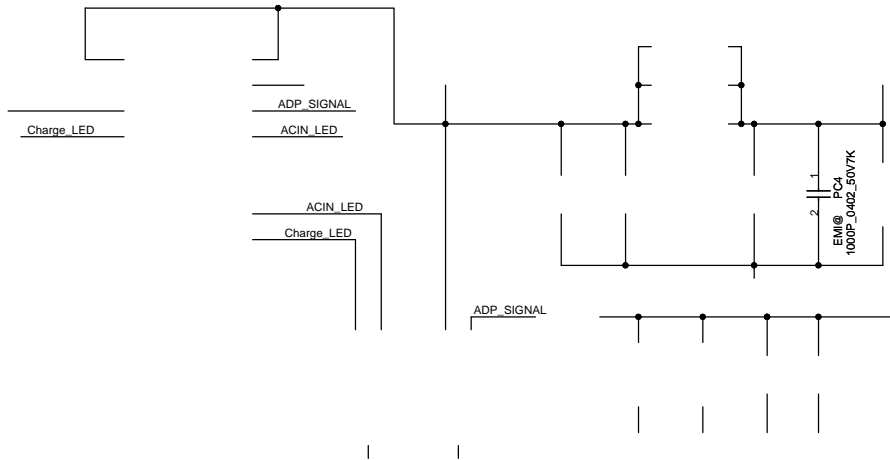
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Headphone and Mic conn

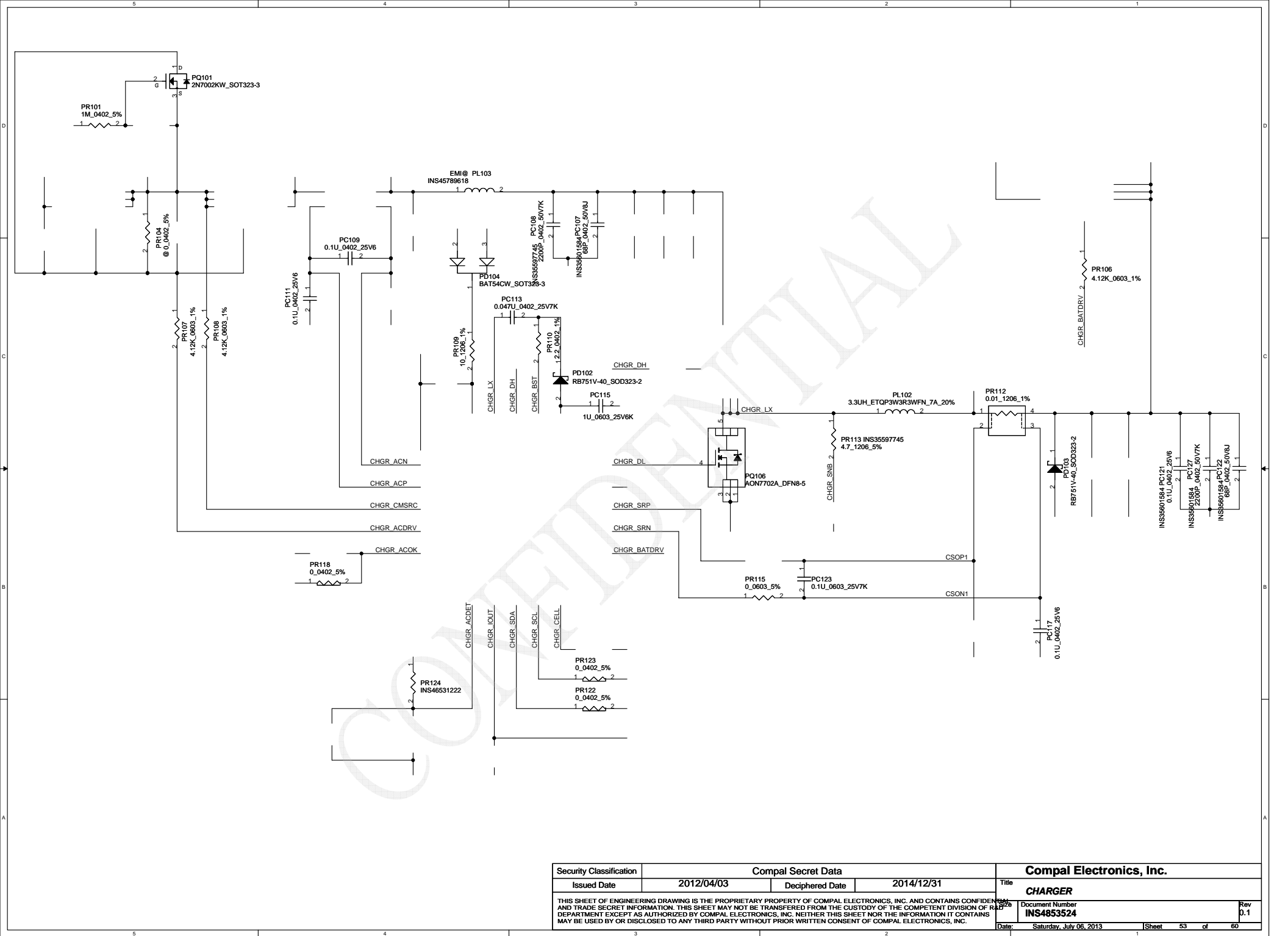
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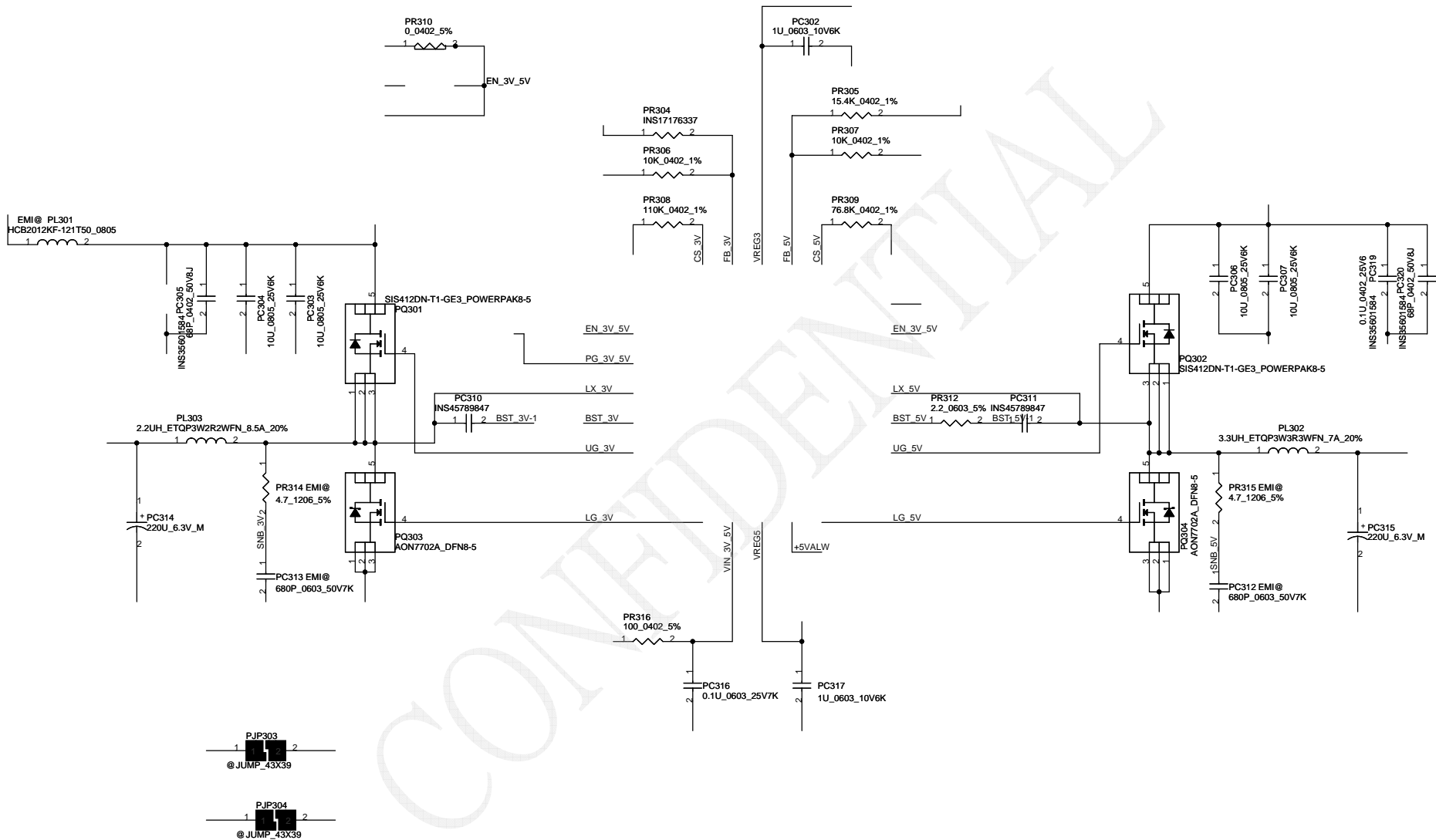
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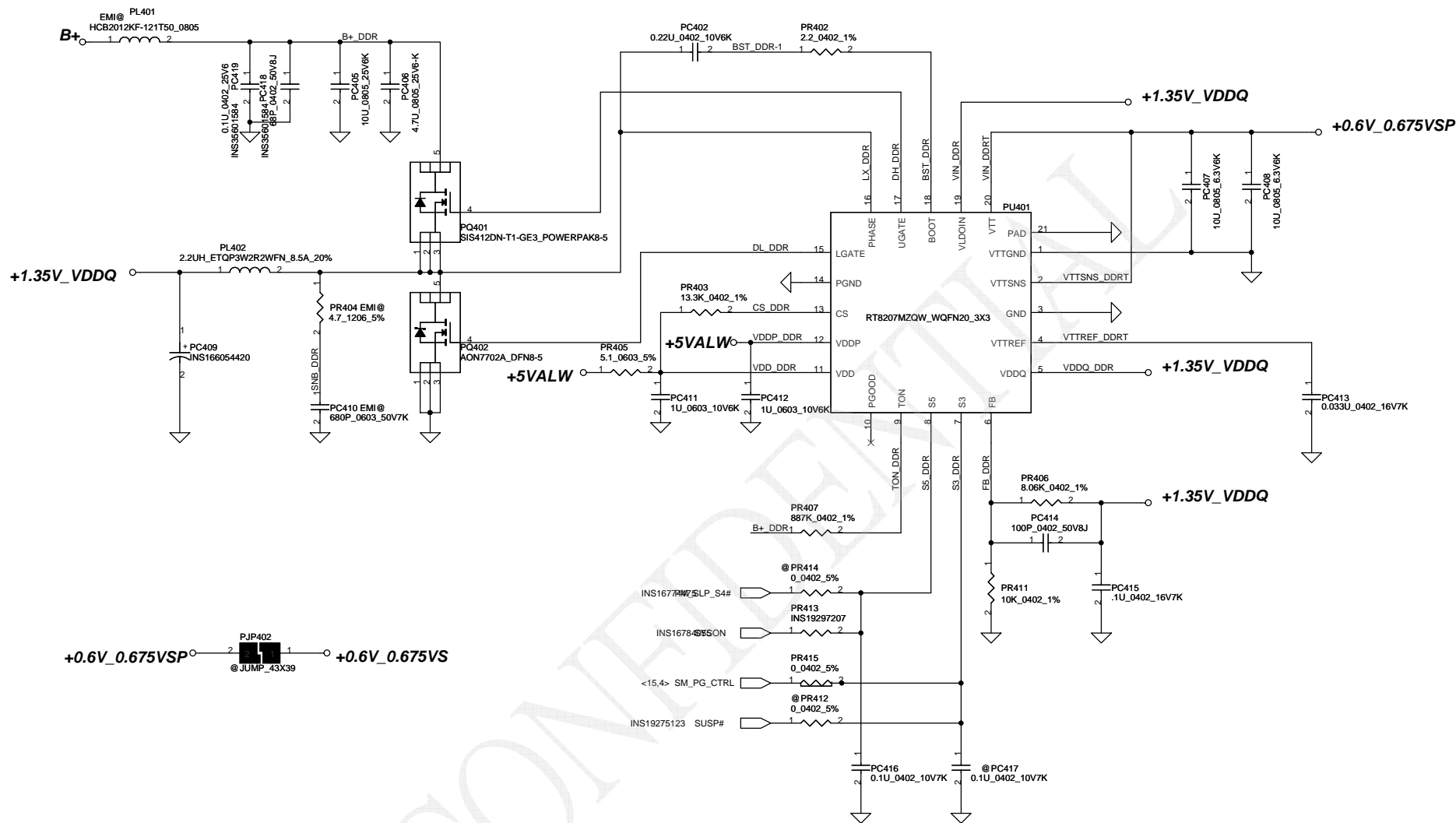
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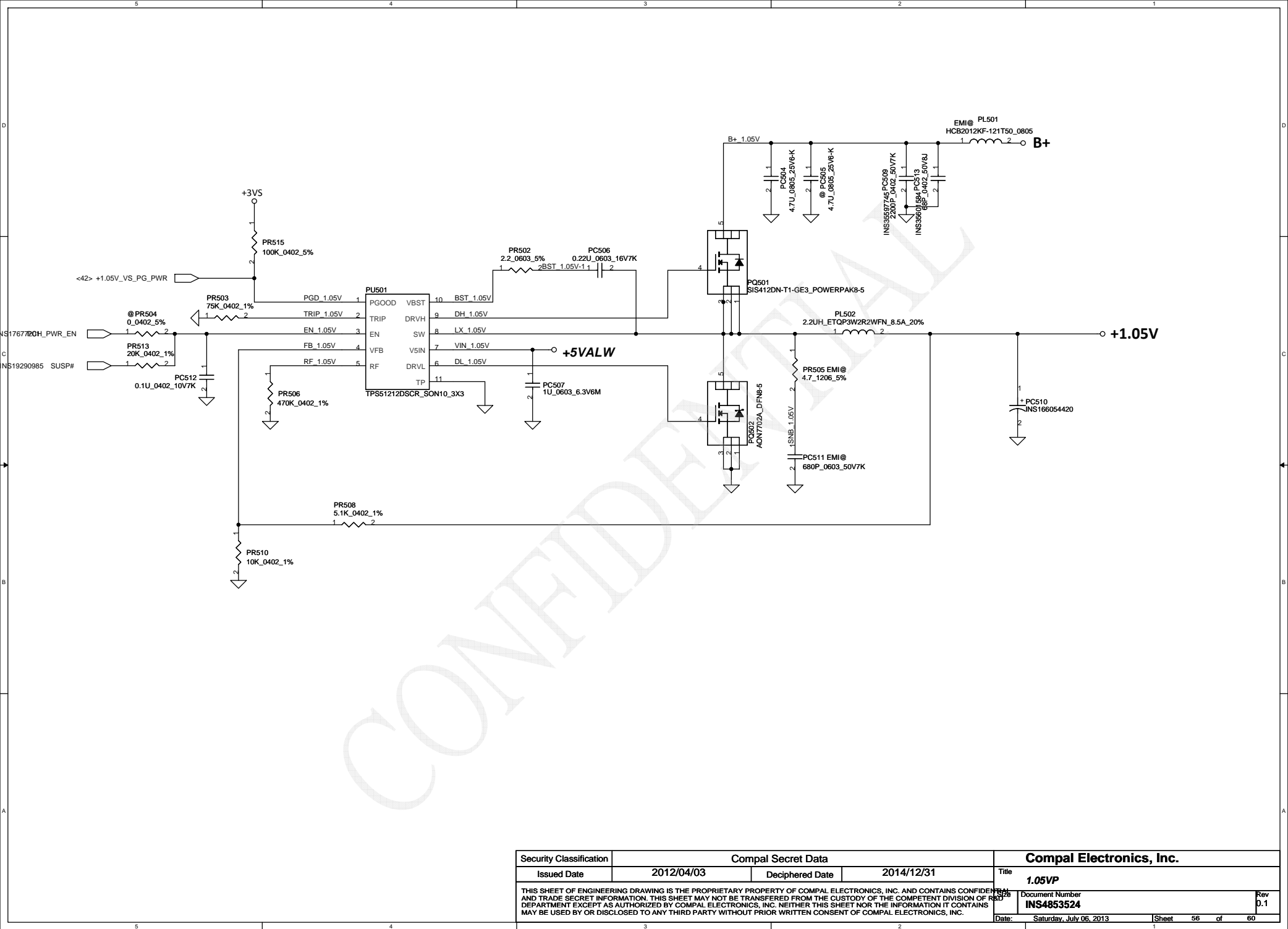
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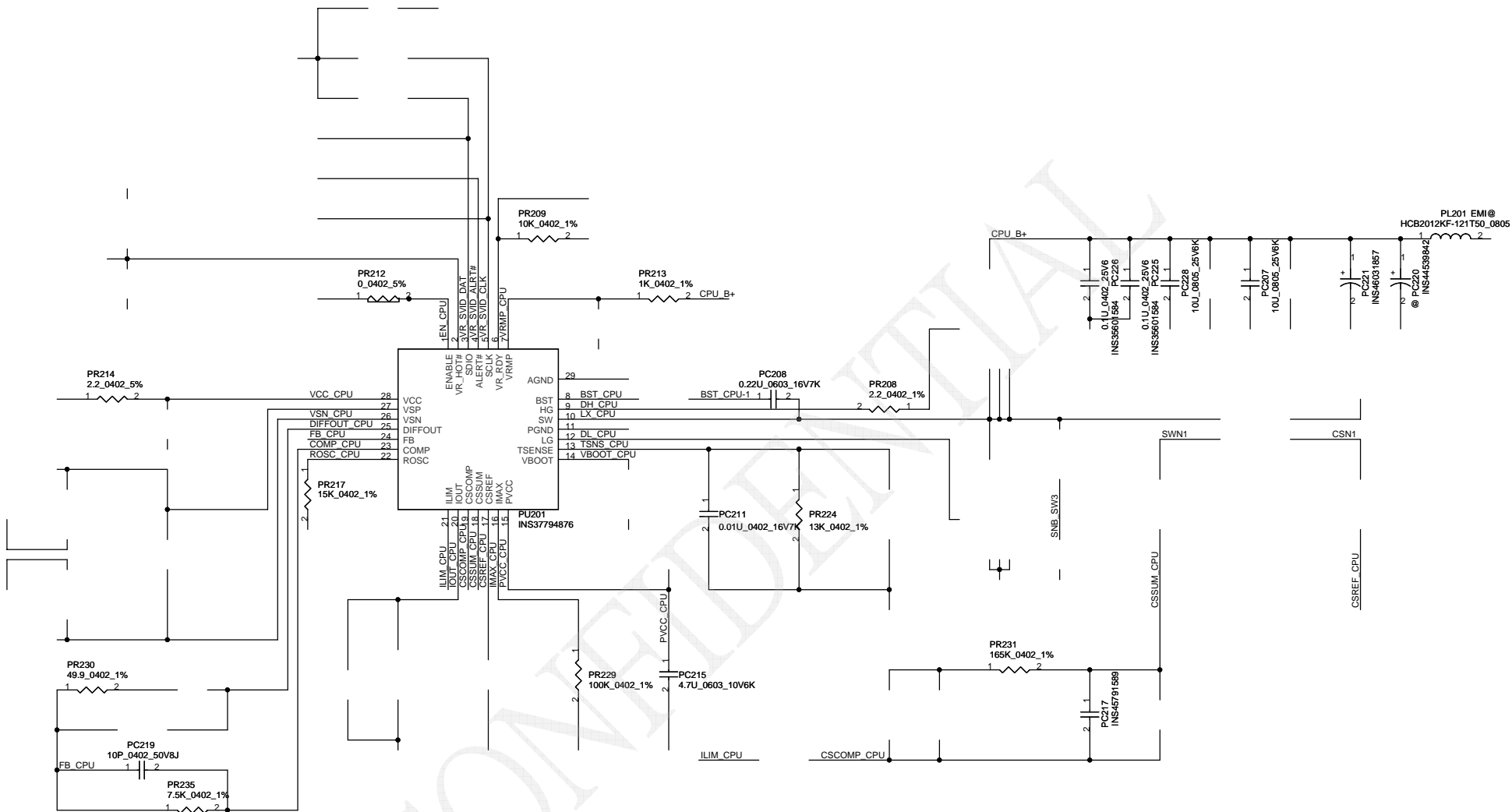
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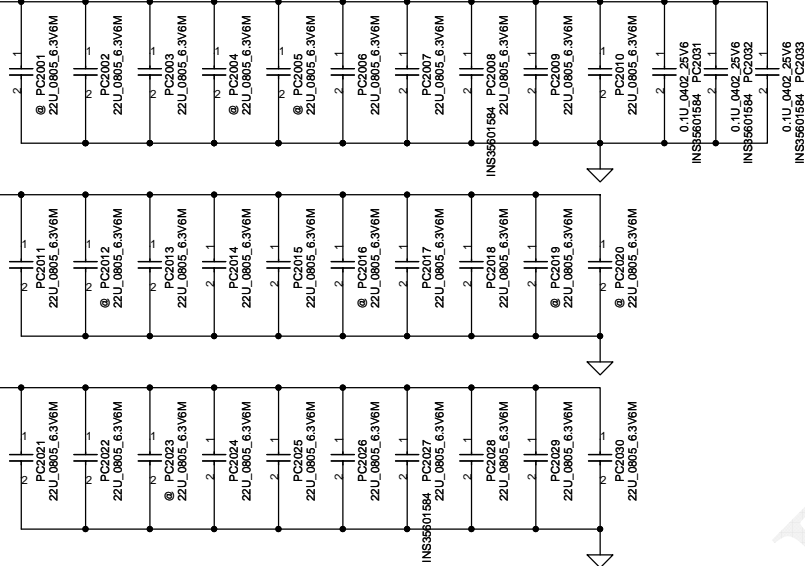
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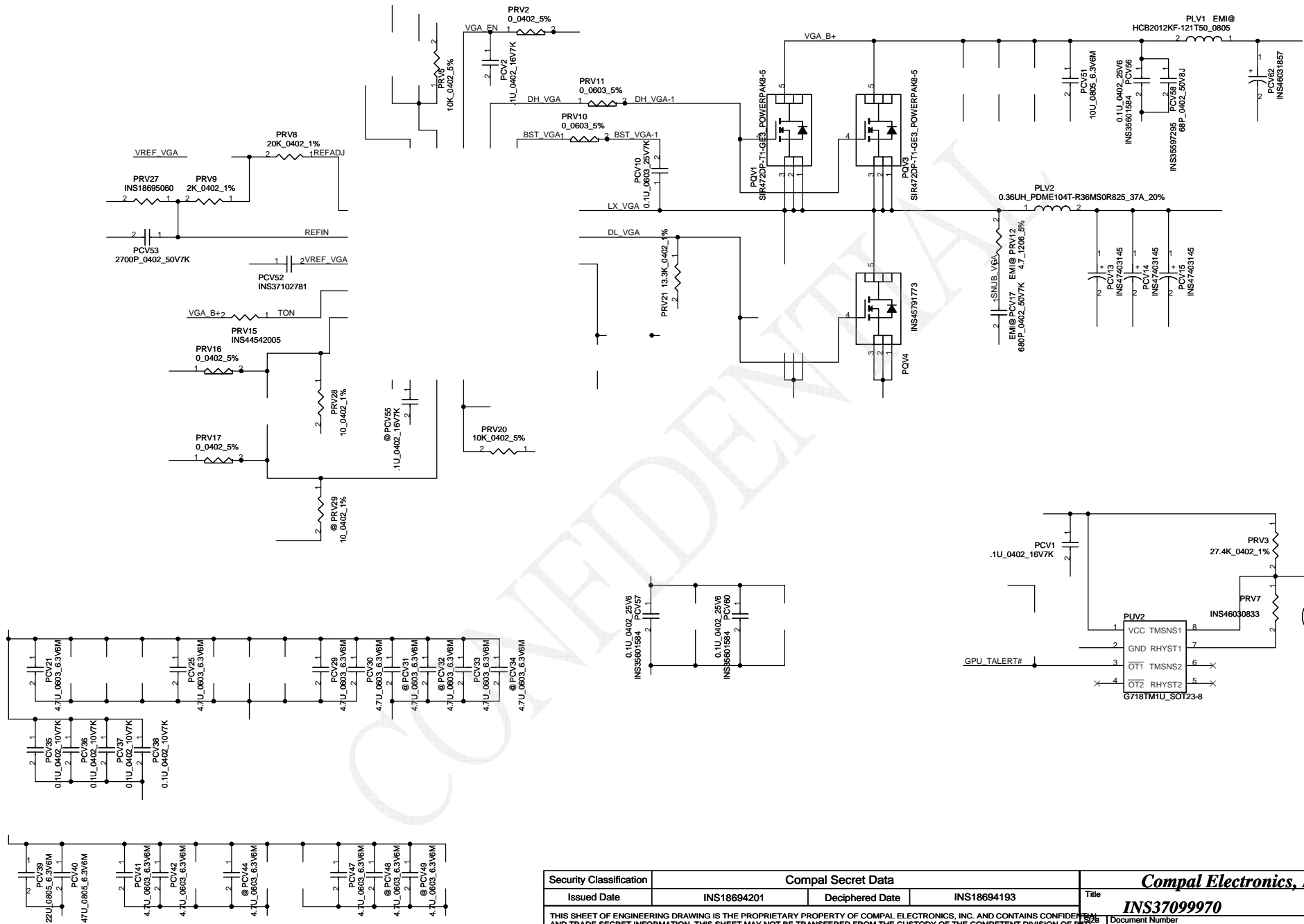
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+VCC_CORE

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